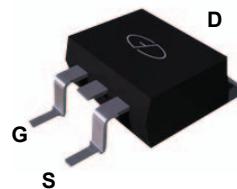
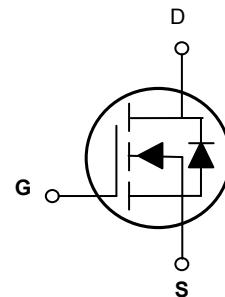


Main Product Characteristics

BV _{DSS}	150V
R _{DS(ON)}	5.8mΩ
I _D	150A



TO-263(D²PAK)



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSGT15150 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supply and a wide variety of other applications.

Absolute Maximum Ratings (T_C=25°C unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	V _{DS}	150	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous (T _C =25°C)	I _D	150	A
Drain Current-Continuous (T _C =100°C)		95	
Drain Current-Pulsed ¹	I _{DM}	600	A
Single Pulse Avalanche Energy ²	E _{AS}	1350	mJ
Single Pulse Avalanche Current ²	I _{AS}	52	A
Power Dissipation (T _C =25°C)	P _D	305	W
Power Dissipation-Derate above 25°C		2.44	W/°C
Thermal Resistance, Junction-to-Ambient	R _{θJA}	62	°C/W
Thermal Resistance, Junction-to-Case	R _{θJC}	0.41	°C/W
Operating Junction Temperature Range	T _J	-55 To +150	°C
Storage Temperature Range	T _{STG}	-55 To +150	°C

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On/Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	150	-	-	V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=120\text{V}, V_{\text{GS}}=0\text{V}$ $T_J=25^\circ\text{C}$	-	-	1	μA
		$V_{\text{DS}}=120\text{V}, V_{\text{GS}}=0\text{V}$ $T_J=85^\circ\text{C}$	-	-	10	
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Static Drain-Source On-Resistance ³	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=30\text{A}$	-	4.8	5.8	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250\mu\text{A}$	2.0	2.8	4.0	V
Forward Transconductance	g_{fs}	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=3\text{A}$	-	18	-	S
Dynamic and Switching Characteristics						
Total Gate Charge ^{3,4}	Q_g	$V_{\text{DS}}=75\text{V}, I_{\text{D}}=80\text{A},$ $V_{\text{GS}}=10\text{V}$	-	130	170	nC
Gate-Source Charge ^{3,4}	Q_{gs}		-	36	55	
Gate-Drain Charge ^{3,4}	Q_{gd}		-	32	50	
Turn-On Delay Time ^{3,4}	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=75\text{V},$ $R_{\text{G}}=6\Omega,$ $V_{\text{GS}}=10\text{V}, I_{\text{D}}=80\text{A}$	-	70	105	nS
Rise Time ^{3,4}	t_r		-	205	310	
Turn-Off Delay Time ^{3,4}	$t_{\text{d}(\text{off})}$		-	402	600	
Fall Time ^{3,4}	t_f		-	197	300	
Input Capacitance	C_{iss}	$V_{\text{DS}}=75\text{V},$ $V_{\text{GS}}=0\text{V}, F=1\text{MHz}$	-	8525	12500	pF
Output Capacitance	C_{oss}		-	700	1050	
Reverse Transfer Capacitance	C_{rss}		-	25	40	
Gate Resistance	R_g	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, F=1\text{MHz}$	-	2.7	-	Ω
Drain-Source Diode Characteristics and Maximum Ratings						
Continuous Source Current	I_s	$V_{\text{G}}=V_{\text{D}}=0\text{V}, \text{Force Current}$	-	-	150	A
Pulsed Source Current	I_{SM}		-	-	300	A
Diode Forward Voltage	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=1\text{A}, T_J=25^\circ\text{C}$	-	-	1	V
Reverse Recovery Time	t_{rr}	$V_R=100\text{V}, I_{\text{s}}=20\text{A},$ $dI/dt=100\text{A}/\mu\text{s}, T_J=25^\circ\text{C}$	-	135	-	nS
Reverse Recovery Charge	Q_{rr}		-	730	-	nC

Note:

- Repetitive rating: Pulsed width limited by maximum junction temperature.
- $V_{\text{DD}}=50\text{V}, V_{\text{GS}}=10\text{V}, L=1\text{mH}, I_{\text{AS}}=52\text{A}, R_{\text{G}}=25\Omega$, starting $T_J=25^\circ\text{C}$.
- Pulse test: pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
- Essentially independent of operation temperature.

Typical Electrical and Thermal Characteristic Curves

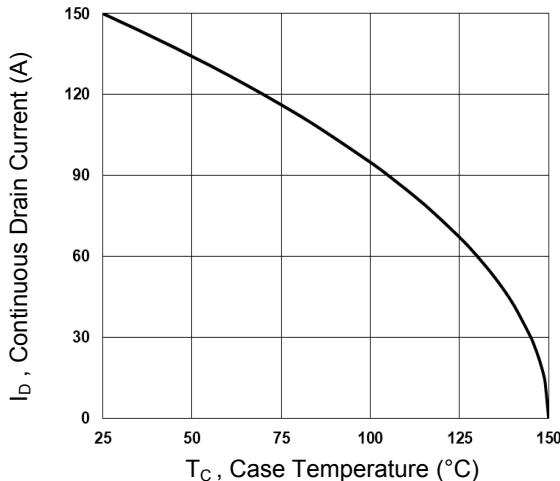


Figure 1. Continuous Drain Current vs. T_c

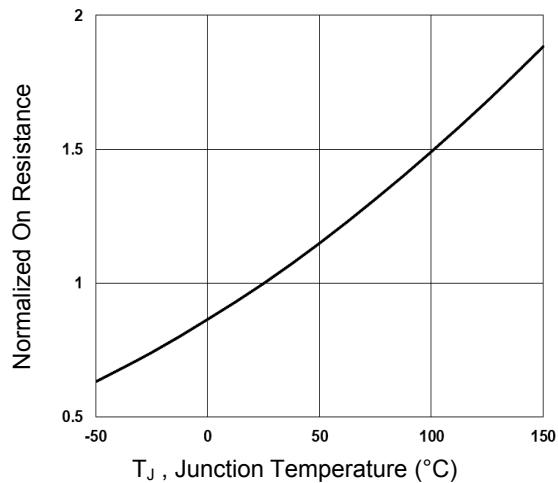


Figure 2. Normalized $R_{D(S(ON))}$ vs. T_j

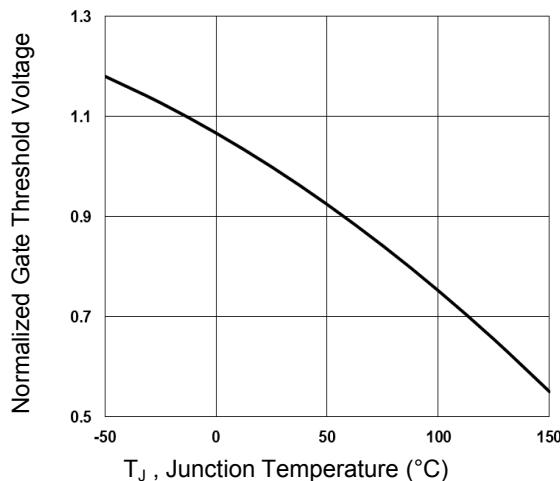


Figure 3. Normalized V_{th} vs. T_j

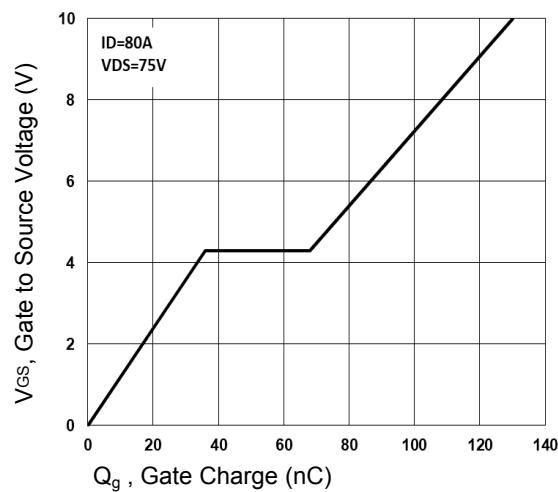


Figure 4. Gate Charge Characteristic

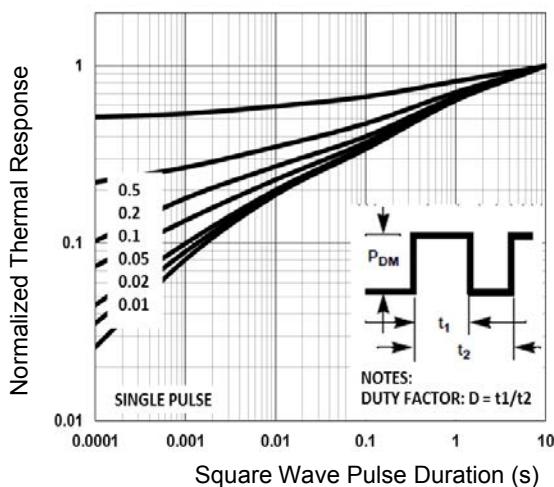


Figure 5. Normalized Transient Impedance

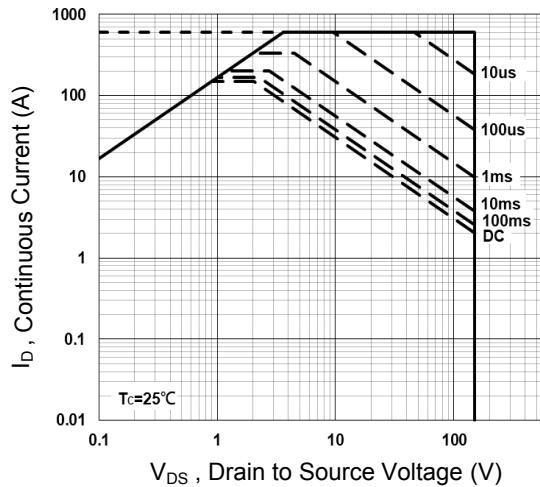


Figure 6. Maximum Safe Operation Area

Typical Electrical and Thermal Characteristic Curves

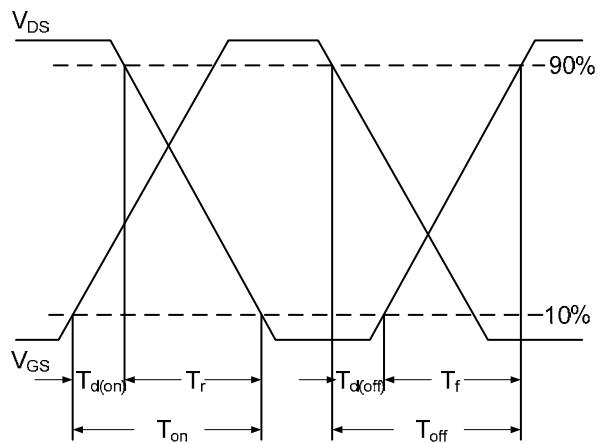


Figure 7. Switching Time Waveform

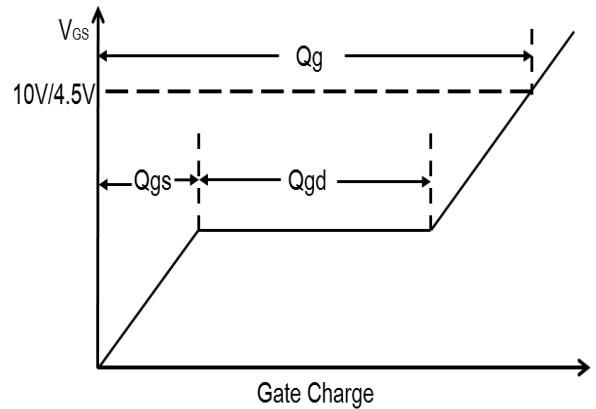


Figure 8. Gate Charge Waveform

Package Outline Dimensions

TO-263(D²PAK)

