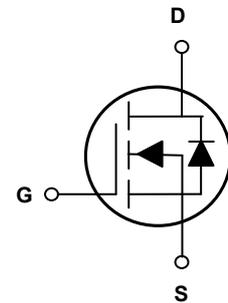
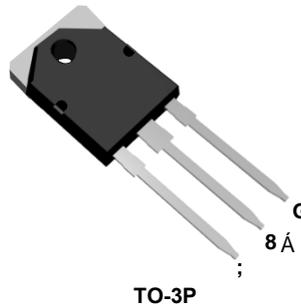


Main Product Characteristics

| | |
|--------------|--------------|
| BV_{DSS} | 150V |
| $R_{DS(ON)}$ | 46m Ω |
| I_D | 30A |



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSFV1530 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise specified)

| Parameter | Symbol | Max. | Unit |
|--|-----------------|-------------|---------------------------|
| Drain-Source Voltage | V_{DS} | 150 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Drain Current-Continuous ($T_c=25^\circ\text{C}$) | I_D | 30 | A |
| Drain Current-Continuous ($T_c=100^\circ\text{C}$) | | 19 | |
| Drain Current-Pulsed ¹ | I_{DM} | 120 | A |
| Single Pulse Avalanche Energy ² | E_{AS} | 29 | mJ |
| Single Pulse Avalanche Current ² | I_{AS} | 24 | A |
| Power Dissipation ($T_c=25^\circ\text{C}$) | P_D | 158 | W |
| Power Dissipation - Derate above 25°C | | 1.27 | |
| Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | 62 | $^\circ\text{C}/\text{W}$ |
| Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | 0.79 | $^\circ\text{C}/\text{W}$ |
| Operating Junction Temperature Range | T_J | -55 To +150 | $^\circ\text{C}$ |
| Storage Temperature Range | T_{STG} | -55 To +150 | $^\circ\text{C}$ |

Electrical Characteristics (T_J=25°C unless otherwise specified)

| Parameter | Symbol | Conditions | Min. | Typ. | Max. | Unit |
|---|---------------------|---|------|------|------|------|
| On / Off Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} =0V, I _D =250uA | 150 | - | - | V |
| Drain-Source Leakage Current | I _{DSS} | V _{DS} =120V, V _{GS} =0V, T _J =25°C | - | - | 1 | μA |
| | | V _{DS} =120V, V _{GS} =0V, T _J =85°C | - | - | 10 | μA |
| Gate-Source Leakage Current | I _{GSS} | V _{GS} =±20V, V _{DS} =0V | - | - | ±100 | nA |
| Static Drain-Source On-Resistance ³ | R _{DS(ON)} | V _{GS} =10V, I _D =20A | - | 38 | 46 | mΩ |
| Gate Threshold Voltage | V _{GS(th)} | V _{GS} =V _{DS} , I _D =250uA | 2.5 | 3.5 | 4.5 | V |
| Forward Transconductance | g _{fs} | V _{DS} =10V, I _D =3A | - | 9 | - | S |
| Dynamic and Switching Characteristics | | | | | | |
| Total Gate Charge ^{3,4} | Q _g | V _{DS} =75V, I _D =15A, V _{GS} =10V | - | 17.4 | - | nC |
| Gate-Source Charge ^{3,4} | Q _{gs} | | - | 2.7 | - | |
| Gate-Drain Charge ^{3,4} | Q _{gd} | | - | 6.1 | - | |
| Turn-On Delay Time ^{3,4} | t _{d(on)} | V _{DD} =75V, R _G =6Ω, V _{GS} =10V, I _D =15A | - | 4.6 | - | nS |
| Rise Time ^{3,4} | t _r | | - | 15 | - | |
| Turn-Off Delay Time ^{3,4} | t _{d(off)} | | - | 27 | - | |
| Fall Time ^{3,4} | t _f | | - | 8 | - | |
| Input Capacitance | C _{iss} | V _{DS} =75V, V _{GS} =0V, F=1MHz | - | 835 | - | pF |
| Output Capacitance | C _{oss} | | - | 75 | - | |
| Reverse Transfer Capacitance | C _{rss} | | - | 7 | - | |
| Gate Resistance | R _g | V _{GS} =0V, V _{DS} =0V, F=1MHz | - | 0.8 | - | Ω |
| Drain-Source Diode Characteristics and Maximum Ratings | | | | | | |
| Continuous Source Current | I _S | V _G =V _D =0V, Force Current | - | - | 30 | A |
| Pulsed Source Current | I _{SM} | | - | - | 60 | A |
| Diode Forward Voltage | V _{SD} | V _{GS} =0V, I _S =1A, T _J =25°C | - | - | 1 | V |

Note:

1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
2. V_{DD}=50V, L=0.1mH, I_{AS}=24A, R_G=25Ω, starting T_J=25°C.
3. Pulse Test: pulse width ≤ 300us, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.

Typical Electrical and Thermal Characteristic Curves

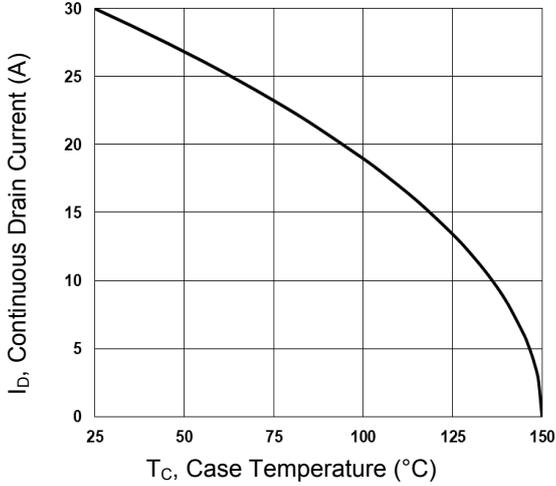


Figure 1. Continuous Drain Current vs. T_c

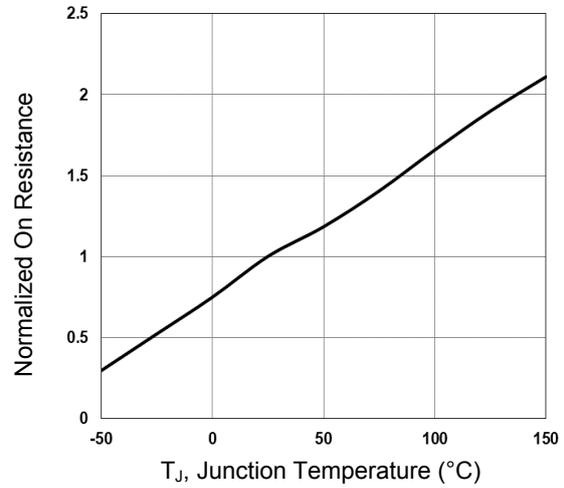


Figure 2. Normalized R_{DS(ON)} vs. T_J

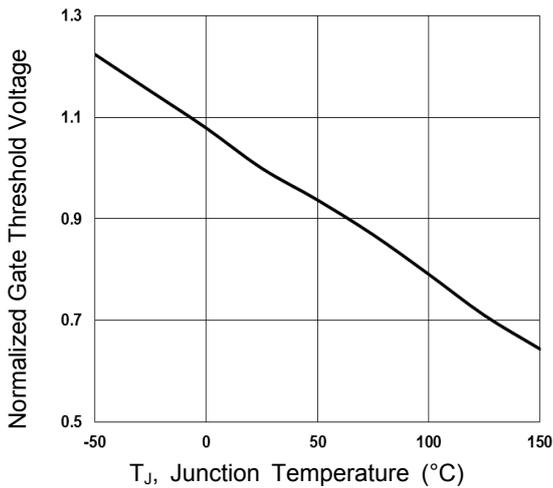


Figure 3. Normalized V_{th} vs. T_J

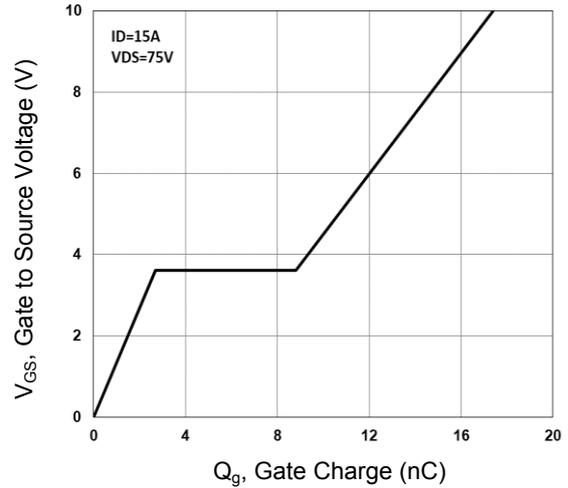


Figure 4. Gate Charge Characteristics

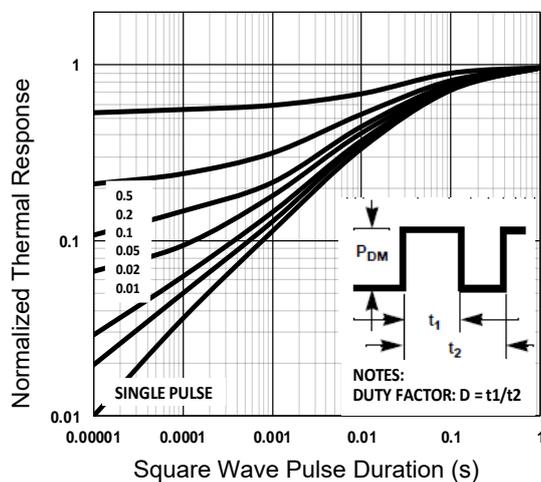


Figure 5. Normalized Transient Impedance

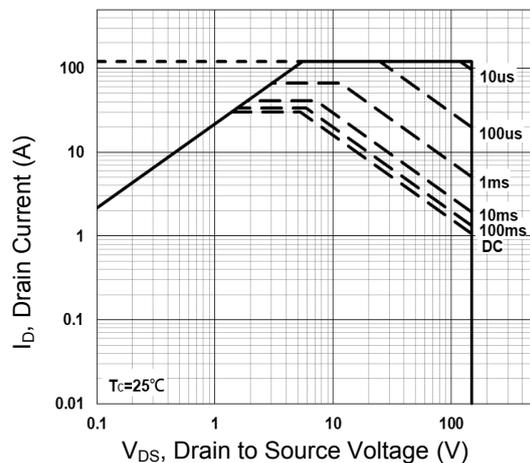


Figure 6. Maximum Safe Operation Area

Typical Electrical and Thermal Characteristic Curves

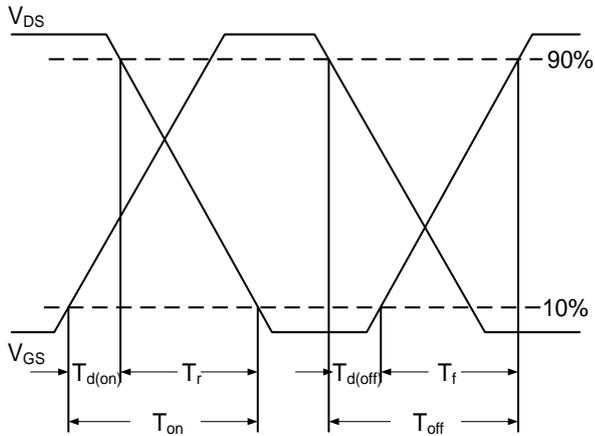


Figure 7. Switching Time Waveform

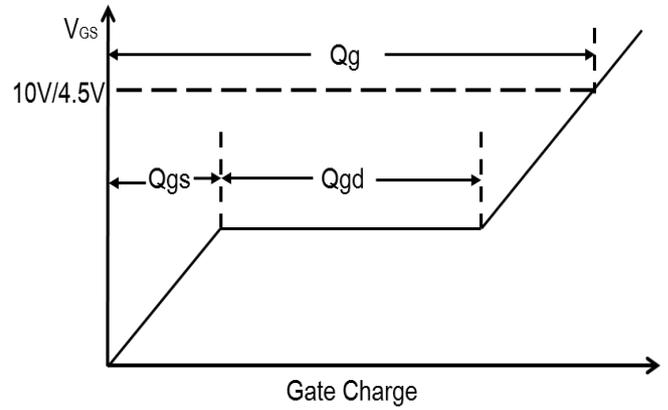
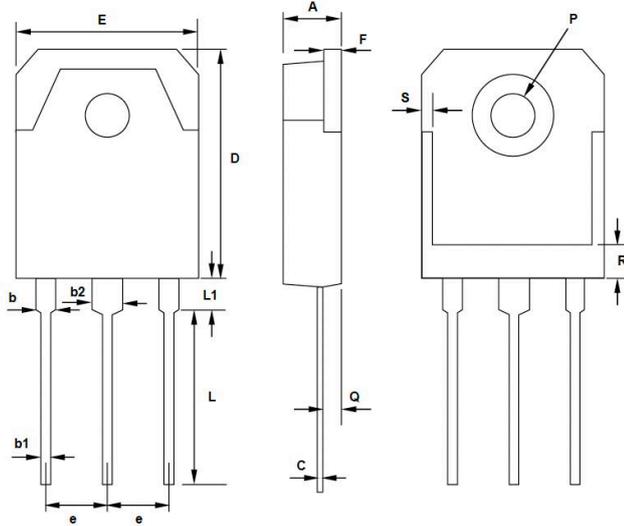


Figure 8. Gate Charge Waveform

Package Outline Dimensions (TO-3P)



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|--------|----------------------|-------|
| | Min | Max | Min | Max |
| A | 4.600 | 5.000 | 0.181 | 0.197 |
| b | 1.800 | 2.200 | 0.071 | 0.087 |
| b1 | 0.900 | 1.100 | 0.035 | 0.043 |
| b2 | 2.800 | 3.200 | 0.110 | 0.126 |
| C | 0.500 | 0.700 | 0.020 | 0.028 |
| D | 19.700 | 20.100 | 0.776 | 0.791 |
| E | 15.400 | 15.800 | 0.606 | 0.622 |
| F | 1.400 | 1.700 | 0.055 | 0.067 |
| e | 5.450 BSC | | 0.215 BSC | |
| L | 16.200 | 16.800 | 0.638 | 0.661 |
| L1 | 3.500 REF | | 0.138 REF | |
| P | 3.300 REF | | 0.130 REF | |
| R | 3.100 REF | | 0.122 REF | |
| S | 1.000 REF | | 0.039 REF | |
| Q | 1.400 REF | | 0.055 REF | |