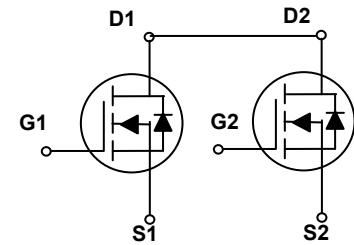
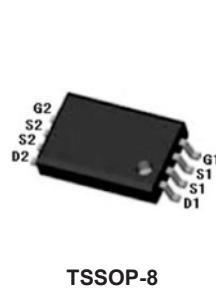


## Main Product Characteristics

$V_{(BR)DSS}$	20V
$R_{DS(ON)}$	26mΩ (max.)
$I_D$	6A



Schematic Diagram



## Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery

## Description

The GSFQ8205A utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

## Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	$V_{DS}$	20	V
Gate-to-Source Voltage	$V_{GS}$	$\pm 10$	V
Continuous Drain Current, @ Steady-State ( $T_A=25^\circ\text{C}$ ) <sup>1</sup>	$I_D$	6.0	A
Continuous Drain Current, @ Steady-State ( $T_A=70^\circ\text{C}$ )		4.8	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	20	A
Power Dissipation ( $T_A=25^\circ\text{C}$ )	$P_D$	1.5	W
Linear Derating Factor ( $T_A=25^\circ\text{C}$ )		0.012	W/ $^\circ\text{C}$
Junction-to-Ambient (PCB Mounted, Steady-State) <sup>3</sup>	$R_{\theta JA}$	83.0	$^\circ\text{C}/\text{W}$
Operating Junction and Storage Temperature Range	$T_J/T_{STG}$	-55 to +150	$^\circ\text{C}$

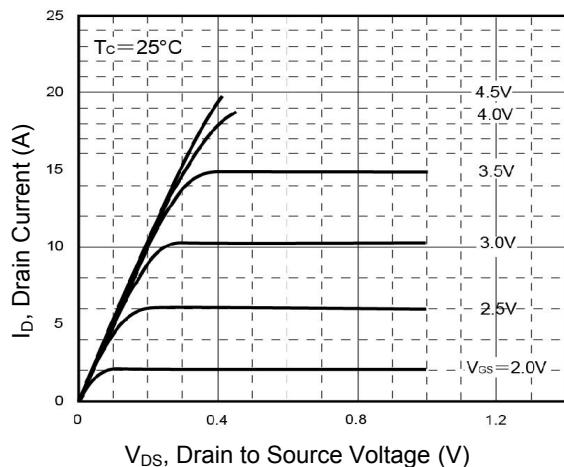
**Electrical Characteristics** ( $T_A=25^\circ\text{C}$  unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>On / Off Characteristics</b>						
Drain-to-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	20	-	-	V
Drain-to-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
		$T_J=125^\circ\text{C}$	-	-	50	
Gate-to-Source Forward Leakage	$I_{\text{GSS}}$	$V_{\text{GS}}=10\text{V}$	-	-	100	$\text{nA}$
		$V_{\text{GS}}=-10\text{V}$	-	-	-100	
Static Drain-to-Source On Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=4.5\text{V}, I_D=4.5\text{A}$	-	22	26	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}, I_D=3.55\text{A}$	-	26	32	
Gate Resistance	$g_{\text{fs}}$	$V_{\text{DS}}=5\text{V}, I_D=4.5\text{A}$	-	10	-	S
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	0.5	0.7	1.2	V
<b>Dynamic and Switching Characteristics</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=8\text{V}$ $F=1\text{MHz}$	-	600	-	$\text{pF}$
Output Capacitance	$C_{\text{oss}}$		-	330	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	140	-	
Total Gate Charge	$Q_g$	$I_D=6\text{A}, V_{\text{DD}}=10\text{V}, V_{\text{GS}}=4.5\text{V}$	-	10	-	$\text{nC}$
Gate-to-Source Charge	$Q_{\text{gs}}$		-	2.3	-	
Gate-to-Drain ("Miller") Charge	$Q_{\text{gd}}$		-	3.0	-	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{GS}}=4.5\text{V}, V_{\text{DD}}=10\text{V}, I_D=1\text{A}, R_{\text{GEN}}=6\Omega$	-	10	-	$\text{nS}$
Rise Time	$t_r$		-	11	-	
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	35	-	
Fall Time	$t_f$		-	30	-	
<b>Source-Drain Ratings and Characteristics</b>						
Continuous Source Current (Body Diode)	$I_s$	MOSFET symbol showing the integral reverse p-n junction diode.	-	-	6	A
Pulsed Source Current (Body Diode)	$I_{\text{SM}}$		-	-	20	A
Diode Forward Voltage	$V_{\text{SD}}$	$I_s=1.7\text{A}, V_{\text{GS}}=0\text{V}$	-	0.8	1.2	V

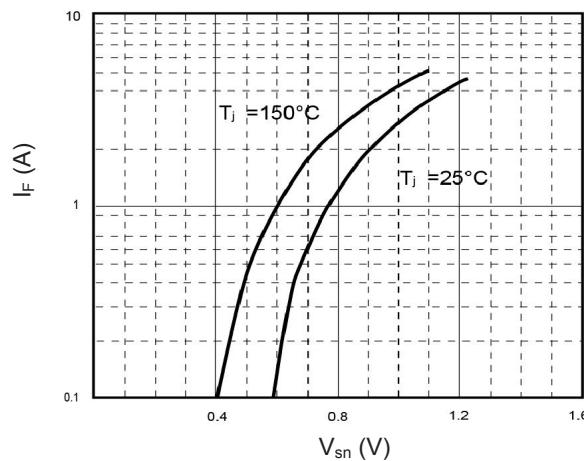
Note:

1. Pulse test: pulse width  $\leq 300\text{us}$ , duty cycle  $\leq 2\%$ .
2. Repetitive rating; pulse width limited by max. junction temperature.
3. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

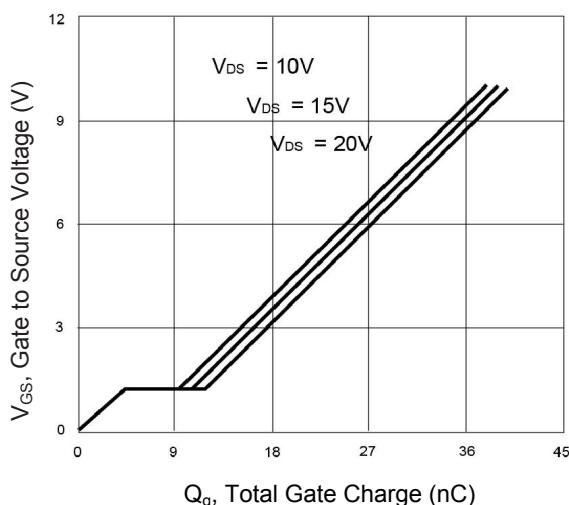
## Typical Electrical and Thermal Characteristic Curves



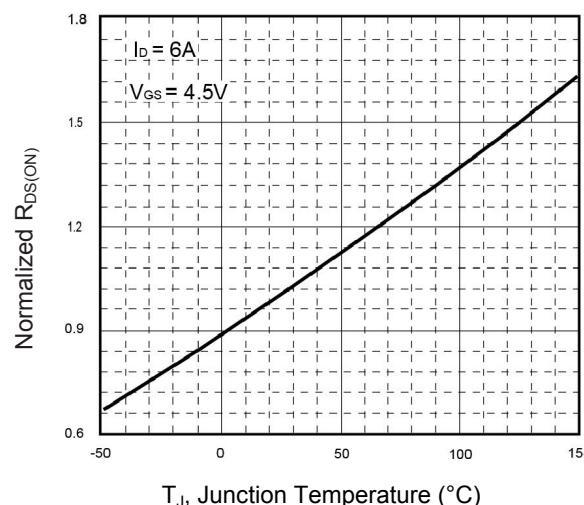
**Figure 1. Output Characteristics**



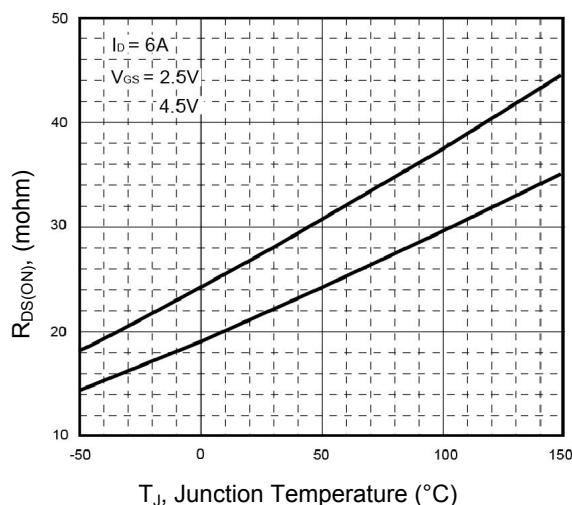
**Figure 2. Body Diode Characteristics**



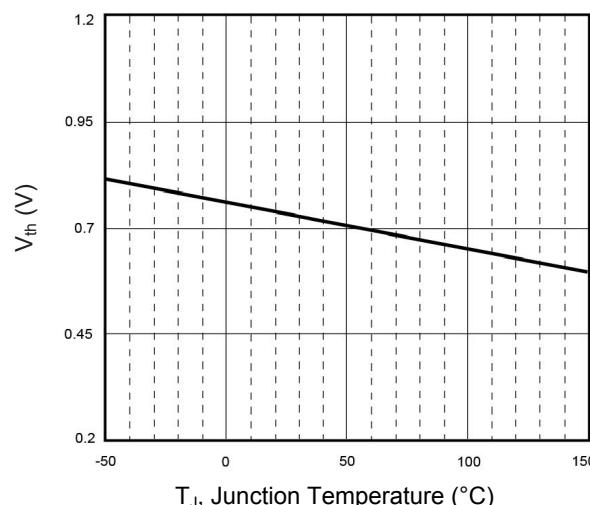
**Figure 3. Gate Charge**



**Figure 4. Normalized  $R_{DS(ON)}$  vs.  $T_j$**



**Figure 5. On-Resistance vs.  $T_j$**



**Figure 6.  $V_{th}$  vs. Junction Temperature**

### Typical Electrical and Thermal Characteristic Curves

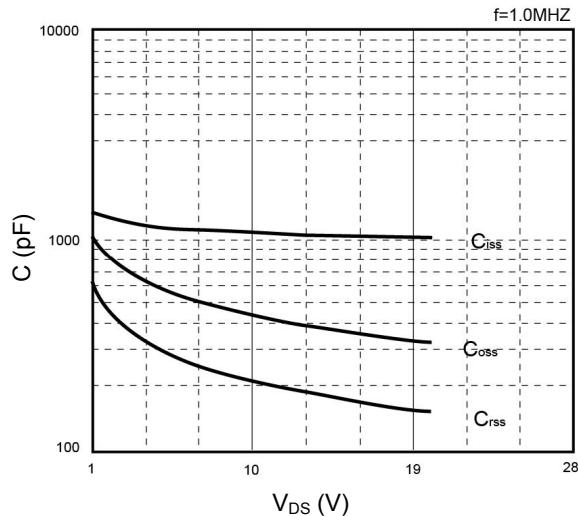


Figure 7. Capacitance Characteristics

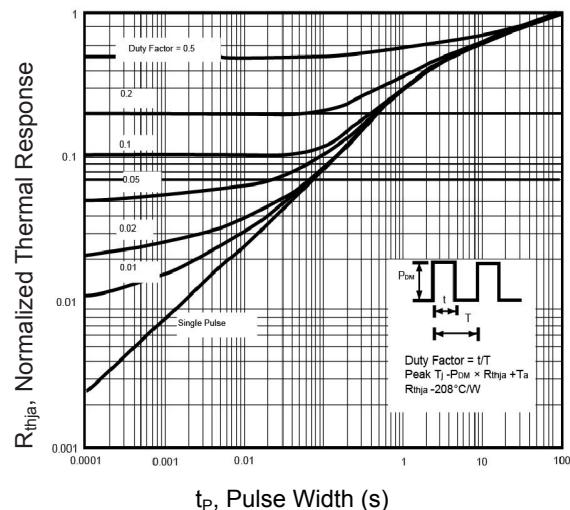


Figure 8. Normalized Transient Impedance

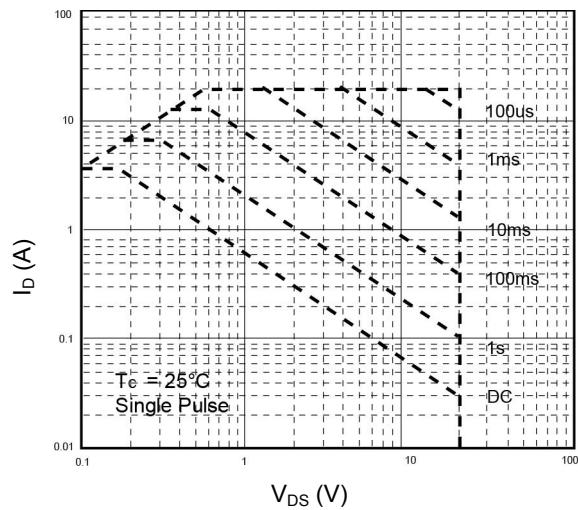
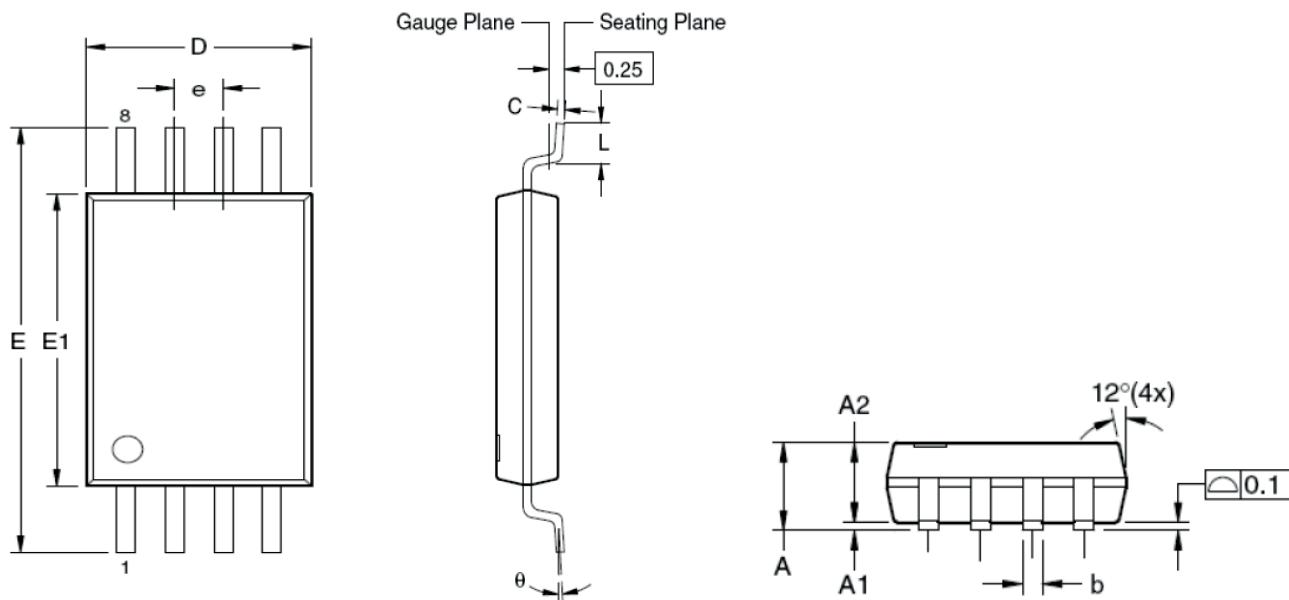


Figure 9. Safe Operation Area

### Package Outline Dimensions (TSSOP-8)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	-	1.20	-	0.047
A1	0.05	0.15	0.002	0.006
A2	0.8	1.05	0.031	0.041
b	0.19	0.30	0.007	0.012
C	0.09	0.20	0.004	0.008
D	2.90	3.10	0.114	0.122
E	6.40 BSC		0.252 BSC	
E1	4.30	4.50	0.169	0.177
e	0.65 BSC		0.026 BSC	
L	0.45	0.75	0.018	0.030
θ	0°	8°	0°	8°