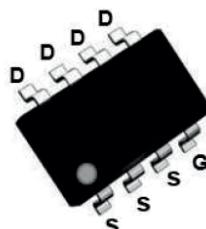
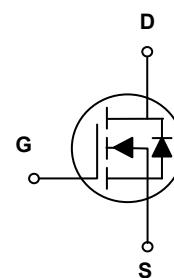


Main Product Characteristics

$V_{(BR)DSS}$	200V
$R_{DS(ON)}$	700mΩ (max.)
I_D	4A



SOP-8



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSFQ2004 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	V_{DS}	200	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous ($T_A=25^\circ\text{C}$)	I_D	4	A
Drain Current-Continuous ($T_A=100^\circ\text{C}$)		2.8	
Drain Current-Pulsed ¹	I_{DM}	16	A
Single Pulse Avalanche Energy ²	E_{AS}	10.6	mJ
Power Dissipation ($T_A=25^\circ\text{C}$)	P_D	3.5	W
Power Dissipation-Derate above 25°C		0.028	W/°C
Thermal Resistance Junction-to-Ambient	$R_{\theta JA}$	45	°C/W
Operating Junction and Storage Temperature Range	T_J/T_{STG}	-55 to +150	°C

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	200	-	-	V
BV_{DSS} Temperature Coefficient	$\triangle \text{BV}_{\text{DSS}}/\triangle T_J$	Reference to 25°C , $I_{\text{D}}=1\text{mA}$	-	0.055	-	$\text{V}/^\circ\text{C}$
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=200\text{V}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$	-	-	1	μA
		$V_{\text{DS}}=160\text{V}, V_{\text{GS}}=0\text{V}, T_J=125^\circ\text{C}$	-	-	10	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=3\text{A}$	-	510	700	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=2\text{A}$	-	550	750	
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250\mu\text{A}$	1.0	1.8	2.9	V
$V_{\text{GS}(\text{th})}$ Temperature Coefficient	$\triangle V_{\text{GS}(\text{th})}$		-	-5	-	$\text{mV}/^\circ\text{C}$
Forward Transconductance	g_{fs}	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=3\text{A}$	-	8	-	S
Dynamic and Switching Characteristics						
Total Gate Charge ^{3,4}	Q_g	$V_{\text{DS}}=100\text{V}, I_{\text{D}}=4\text{A}, V_{\text{GS}}=10\text{V}$	-	12.6	-	nC
Gate-Source Charge ^{3,4}	Q_{gs}		-	2.1	-	
Gate-Drain Charge ^{3,4}	Q_{gd}		-	2.2	-	
Turn-On Delay Time ^{3,4}	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=100\text{V}, R_{\text{G}}=5\Omega, V_{\text{GS}}=10\text{V}, I_{\text{D}}=4\text{A}$	-	13	-	nS
Rise Time ^{3,4}	t_r		-	10	-	
Turn-Off Delay Time ^{3,4}	$t_{\text{d}(\text{off})}$		-	40	-	
Fall Time ^{3,4}	t_f		-	10	-	
Input Capacitance	C_{iss}	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}, F=1\text{MHz}$	-	775	-	pF
Output Capacitance	C_{oss}		-	12.5	-	
Reverse Transfer Capacitance	C_{rss}		-	4.4	-	
Gate Resistance	R_g	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, F=1\text{MHz}$	-	1.4	3	Ω
Source-Drain Ratings and Characteristics						
Continuous Source Current	I_s	$V_G=V_D=0\text{V}$, Force Current	-	-	4	A
Pulsed Source Current	I_{SM}		-	-	16	A
Diode Forward Voltage	V_{SD}	$V_{\text{GS}}=0\text{V}, I_s=2\text{A}, T_J=25^\circ\text{C}$	-	-	1.2	V
Reverse Recovery Time ³	T_{rr}	$V_R=50\text{V}, I_s=4\text{A}, di/dt=100\text{A}/\mu\text{s}, T_J=25^\circ\text{C}$	-	86	-	nS
Reverse Recovery Charge ³	Q_{rr}		-	280	-	nC

Notes

- Repetitive rating: Pulsed width limited by maximum junction temperature.
- $V_{\text{DD}}=40\text{V}, V_{\text{GS}}=10\text{V}, L=0.5\text{mH}, R_{\text{G}}=25\Omega$, starting $T_J=25^\circ\text{C}$.
- Pulse test: pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
- Essentially independent of operation temperature.

Typical Electrical and Thermal Characteristic Curves

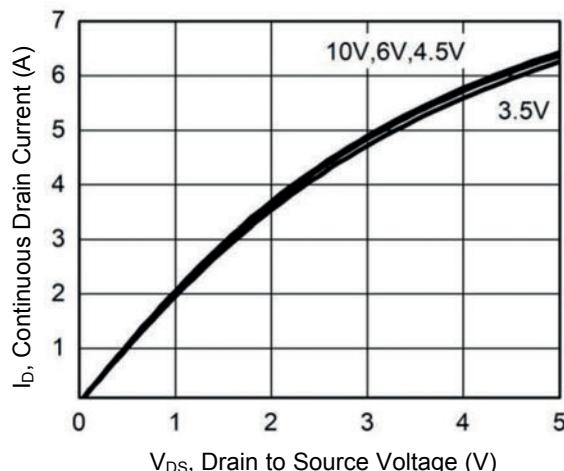


Figure 1. Typical Output Characteristics

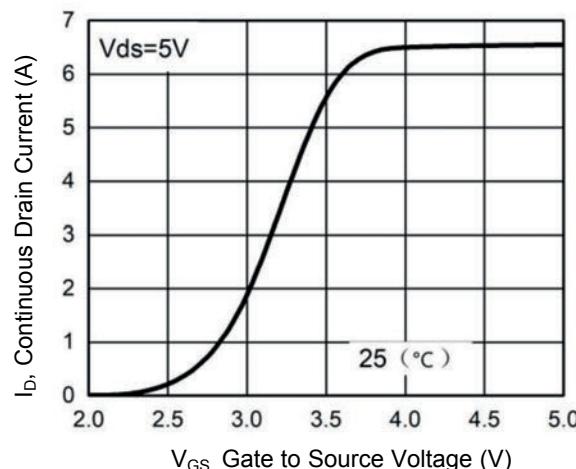


Figure 2. Transfer Characteristics

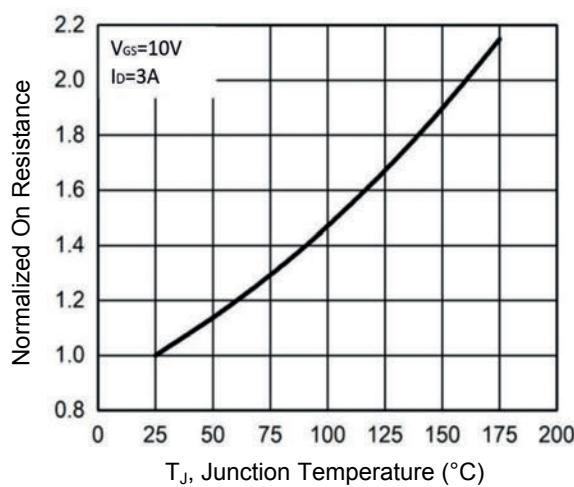


Figure 3. Normalized $R_{DS(on)}$ vs. T_J

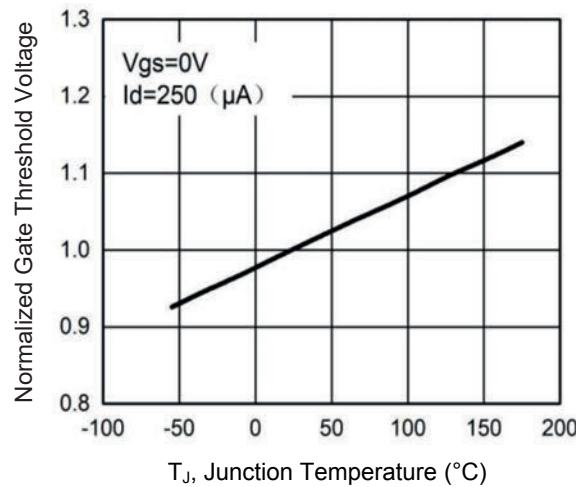


Figure 4. Normalized BV_{DSs} vs. T_J

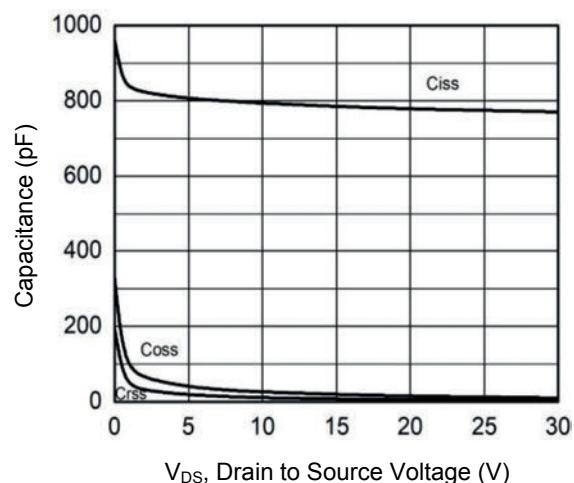


Figure 5. Capacitance Characteristics

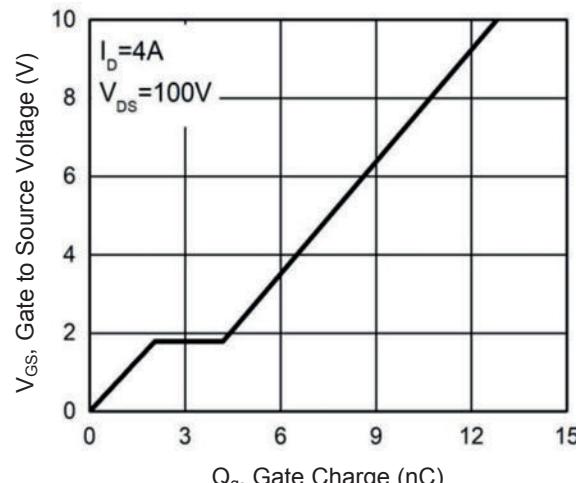


Figure 6. Gate Charge Characteristics

Typical Electrical and Thermal Characteristic Curves

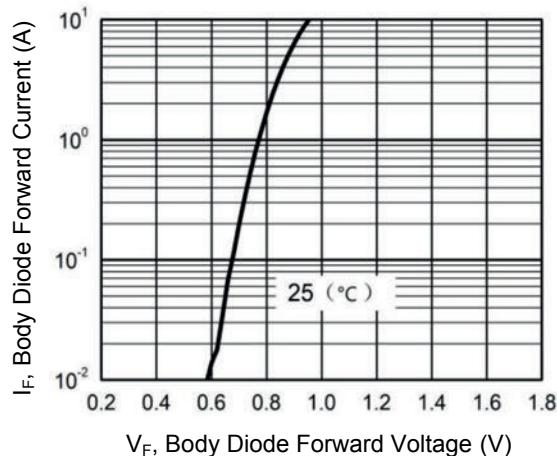


Figure 7. Body Diode Characteristics

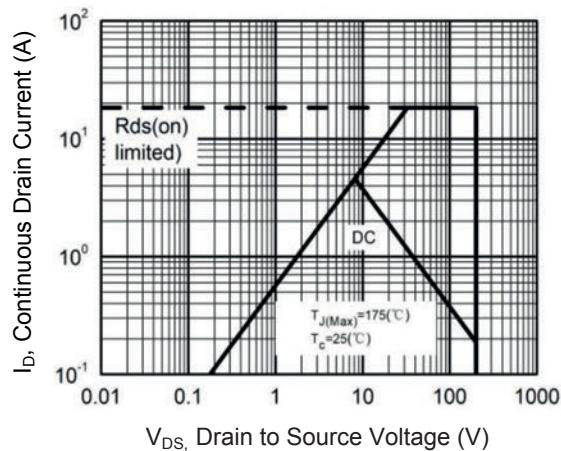
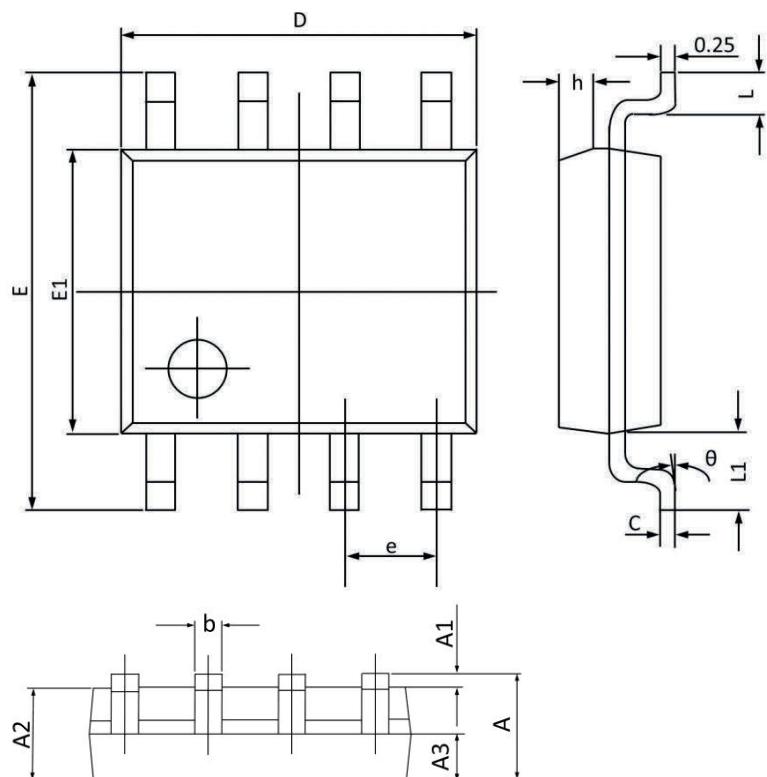


Figure 8. Maximum Safe Operation Area

Package Outline Dimensions (SOP-8)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.068
A1	0.100	0.250	0.004	0.009
A2	1.300	1.500	0.052	0.059
A3	0.600	0.700	0.024	0.027
b	0.390	0.480	0.016	0.018
c	0.210	0.260	0.009	0.010
D	4.700	5.100	0.186	0.200
E	5.800	6.200	0.229	0.244
E1	3.700	4.100	0.146	0.161
e	1.270 (BSC)		0.050 (BSC)	
h	0.250	0.500	0.010	0.019
L	0.500	0.800	0.019	0.031
L1	1.050 (BSC)		0.041 (BSC)	
θ	0°	8°	0°	8°