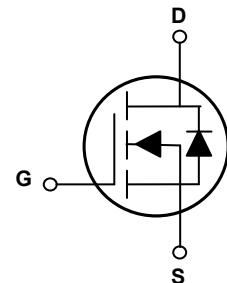


Main Product Characteristics

$V_{(BR)DSS}$	60V
$R_{DS(ON)}$	11mΩ (max.)
I_D	60A



PPAK3x3



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSFN11006 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous @ Steady-State ¹ ($T_C=25^\circ\text{C}$)	I_D	60	A
Drain Current-Continuous @ Steady-State ($T_C=100^\circ\text{C}$)		42.5	
Drain Current-Pulsed ²	I_{DM}	240	A
Power Dissipation ($T_C=25^\circ\text{C}$)	P_D	54	W
Linear Derating Factor ($T_C=25^\circ\text{C}$)		0.44	W/ $^\circ\text{C}$
Single Pulse Avalanche Energy ³	E_{AS}	81	mJ
Junction-to-Case	$R_{\theta JC}$	2.31	$^\circ\text{C}/\text{W}$
Junction-to-Ambient (PCB Mounted, Steady-State) ⁴	$R_{\theta JA}$	62	$^\circ\text{C}/\text{W}$
Operating Junction Temperature Range	T_J	-55 To +150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 To +150	$^\circ\text{C}$

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	60	-	-	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=60\text{V}, V_{GS}=0\text{V}$	-	-	1	μA
		$T_J=125^\circ\text{C}$	-	-	20	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20\text{V}$	-	-	± 100	nA
Static Drain-Source On-Resistance	$R_{DS(\text{ON})}$	$V_{GS}=10\text{V}, I_D=12\text{A}$	-	9.8	11	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=9\text{A}$	-	11	14	$\text{m}\Omega$
Forward Transconductance	g_{fs}	$V_{DS}=10\text{V}, I_D=6\text{A}$	-	12	-	S
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{GS}=V_{DS}, I_D=250\mu\text{A}$	1.2	1.7	2.5	V
Dynamic and Switching Characteristics						
Total Gate Charge	Q_g	$V_{DS}=48\text{V}, I_D=13\text{A}$ $V_{GS}=10\text{V}$	-	18	-	nC
Gate-Source Charge	Q_{gs}		-	5.6	-	
Gate-to-Drain ("Miller") Charge	Q_{gd}		-	2.7	-	
Turn-On Delay Time	$t_{d(\text{on})}$	$V_{DS}=30\text{V}, R_G=3\Omega$ $V_{GS}=10\text{V}, I_D=13\text{A}$	-	8.6	-	nS
Rise Time	t_r		-	53	-	
Turn-Off Delay Time	$t_{d(\text{off})}$		-	18	-	
Fall Time	t_f		-	8.9	-	
Input Capacitance	C_{iss}	$V_{DS}=30\text{V}, V_{GS}=0\text{V},$ $F=1\text{MHz}$	-	1066	2200	pF
Output Capacitance	C_{oss}		-	434	870	
Reverse Transfer Capacitance	C_{rss}		-	26	54	
Gate Resistance	R_g	$F=1\text{MHz}$	-	1.7	3	Ω
Drain-Source Diode Characteristics and Maximum Ratings						
Continuous Source Current (Body Diode)	I_S	MOSFET symbol showing the integral reverse p-n junction diode.	-	-	60	A
Pulsed Source Current (Body Diode)	I_{SM}	$I_F=13\text{A},$ $dI/dt=100\text{A}/\mu\text{s},$ $T_J=25^\circ\text{C}$	-	-	240	A
Diode Forward Voltage	V_{SD}		-	1	1.3	V
Reverse Recovery Time	T_{rr}		-	54	-	nS
Reverse Recovery Charge	Q_{rr}		-	52	-	nC

Note:

1. Pulse test: pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
2. Repetitive rating: Pulsed width limited by maximum junction temperature.
3. $L=0.5\text{mH}, R_G=10\Omega, V_{DD}=50\text{V}, T_J=25^\circ\text{C}$.
4. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

Typical Electrical and Thermal Characteristic Curves

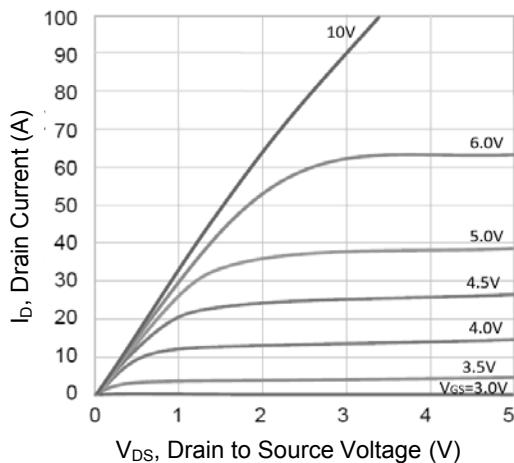


Figure 1. Output Characteristics

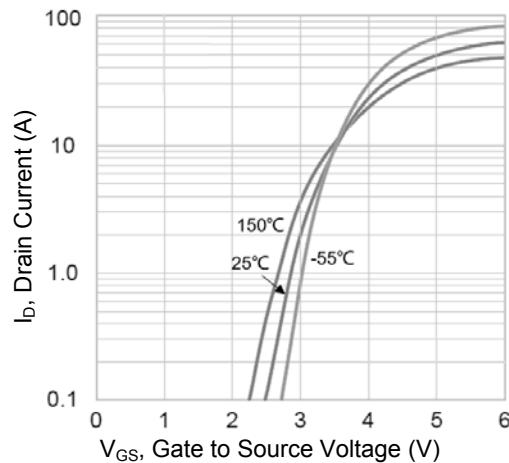


Figure 2. Transfer Characteristics

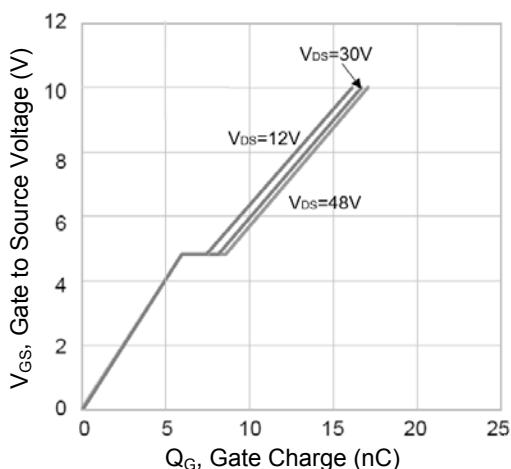


Figure 3. Gate Charge Characteristics

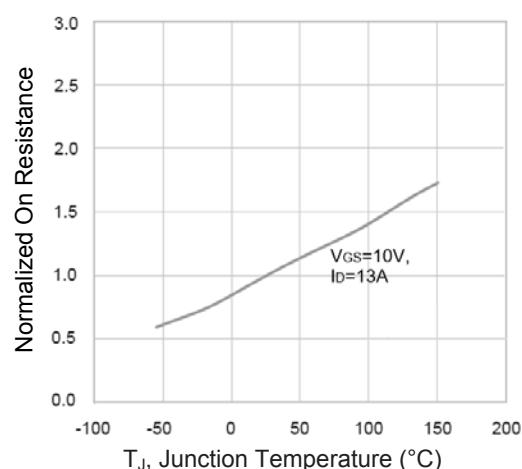


Figure 4. Normalized $R_{DS(ON)}$ Vs. T_J

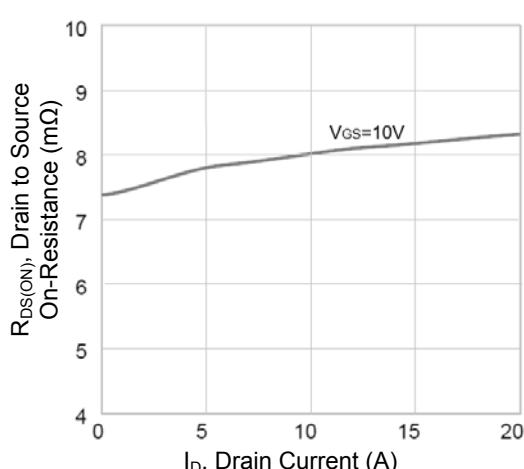


Figure 5. On-Resistance Vs. Drain Current

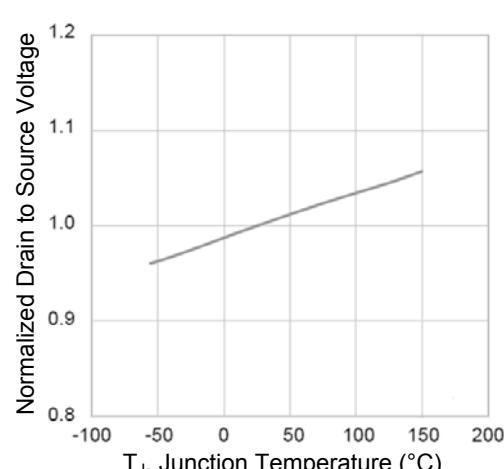


Figure 6. Normalized BV_{dss} Vs. T_J

Typical Electrical and Thermal Characteristic Curves

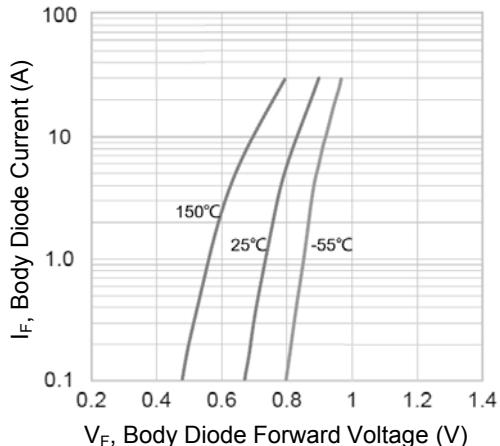


Figure 7. Body Diode Characteristics

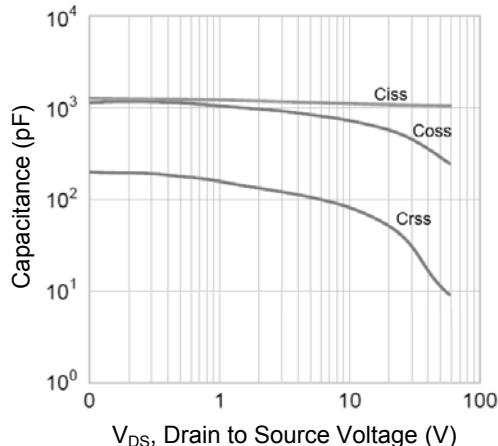


Figure 8. Capacitance Characteristics

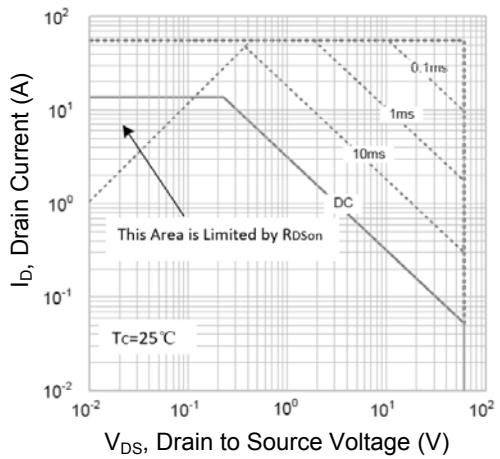
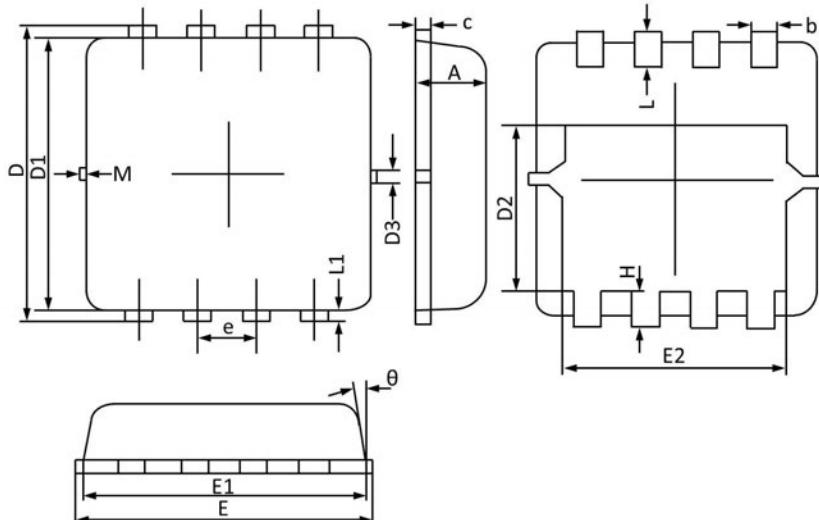


Figure 9. Safe Operation Area

Package Outline Dimensions (PPAK3x3)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.700	0.800	0.028	0.031
b	0.250	0.350	0.010	0.014
c	0.100	0.250	0.004	0.010
D	3.250	3.450	0.128	0.136
D1	3.000	3.200	0.118	0.126
D2	1.780	1.980	0.070	0.078
D3	0.130 REF		0.005 REF	
E	3.200	3.400	0.126	0.134
E1	3.000	3.300	0.118	0.130
E2	2.390	2.590	0.094	0.102
e	0.650 BSC		0.026 BSC	
H	0.300	0.500	0.012	0.020
L	0.300	0.500	0.012	0.020
L1	0.130 REF		0.005 REF	
θ	0°	12°	0°	12°
M	0.150 REF		0.006 REF	