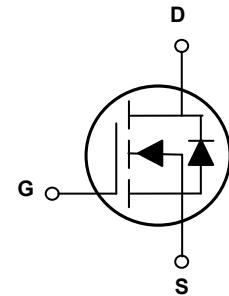
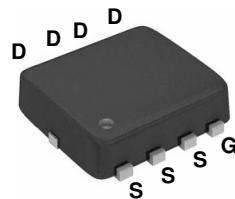


Main Product Characteristics

$V_{(BR)DSS}$	40V
$R_{DS(ON)}$	13mΩ
I_D	30A



PPAK3X3

Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSFN0430 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supply and a wide variety of other applications.

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	+20/-12	V
Drain Current-Continuous($T_C=25^\circ\text{C}$)	I_D	30	A
Drain Current-Continuous($T_C=100^\circ\text{C}$)		19	
Drain Current-Pulsed ¹	I_{DM}	120	A
Single Pulse Avalanche Energy ²	E_{AS}	29	mJ
Single Pulse Avalanche Current ²	I_{AS}	24	A
Power Dissipation($T_C=25^\circ\text{C}$)	P_D	26	W
Power Dissipation-Derate above 25°C		0.21	W/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	4.8	°C/W
Storage Temperature Range	T_{STG}	-55 To +150	°C
Operating Junction Temperature Range	T_J	-55 To +150	°C

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On/Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	40	-	-	V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$	-	-	1	μA
		$V_{\text{DS}}=32\text{V}, V_{\text{GS}}=0\text{V}, T_J=100^\circ\text{C}$	-	-	10	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	100	nA
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=4\text{A}$	-	11	13	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=3\text{A}$	-	17	22	
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250\mu\text{A}$	1.2	1.6	2.5	V
Forward Transconductance	g_{fs}	$V_{\text{DS}}=10\text{V}, I_{\text{S}}=5\text{A}$	-	4	-	S
Dynamic and Switching Characteristics						
Total Gate Charge ^{3,4}	Q_g	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=15\text{A}, V_{\text{GS}}=10\text{V}$	-	6.9	10	nC
Gate-Source Charge ^{3,4}	Q_{gs}		-	1.3	2	
Gate-Drain Charge ^{3,4}	Q_{gd}		-	3	4.5	
Turn-On Delay Time ^{3,4}	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=20\text{V}, R_{\text{G}}=6\Omega, V_{\text{GS}}=10\text{V}, I_{\text{D}}=10\text{A}$	-	8	12	nS
Rise Time ^{3,4}	t_r		-	12	18	
Turn-Off Delay Time ^{3,4}	$t_{\text{d}(\text{off})}$		-	25	38	
Fall Time ^{3,4}	t_f		-	18	27	
Input Capacitance	C_{iss}		-	495	740	pF
Output Capacitance	C_{oss}	$V_{\text{DS}}=20\text{V}, V_{\text{GS}}=0\text{V}, F=1\text{MHz}$	-	310	460	
Reverse Transfer Capacitance	C_{rss}		-	13.5	20	
Gate Resistance	R_g	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, F=1\text{MHz}$	-	2	-	Ω
Drain-Source Diode Characteristics and Maximum Ratings						
Continuous Source Current	I_{S}	$V_G=V_D=0\text{V}, \text{Force Current}$	-	-	30	A
Pulsed Source Current	I_{SM}		-	-	60	A
Diode Forward Voltage	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=1\text{A}, T_J=25^\circ\text{C}$	-	-	1	V
Reverse Recovery Time	t_{rr}	$V_{\text{GS}}=10\text{V}, I_{\text{S}}=10\text{A}, \text{di/dt}=100\text{A}/\mu\text{s}, T_J=25^\circ\text{C}$	-	15	-	nS
Reverse Recovery Charge	Q_{rr}		-	11	-	nC

Note:

- Repetitive Rating: Pulsed width limited by maximum junction temperature.
- $V_{\text{DD}}=25\text{V}, V_{\text{GS}}=10\text{V}, L=0.1\text{mH}, I_{\text{AS}}=24\text{A}, R_{\text{G}}=25\Omega$, Starting $T_J=25^\circ\text{C}$.
- Pulse test: pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
- Essentially independent of operating temperature.

Typical Electrical and Thermal Characteristic Curves

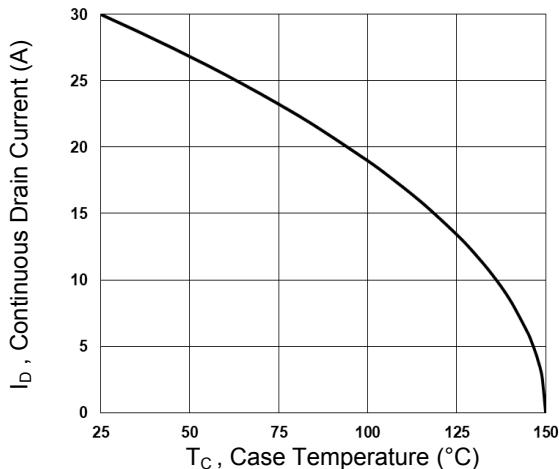


Figure 1. Continuous Drain Current vs. T_c

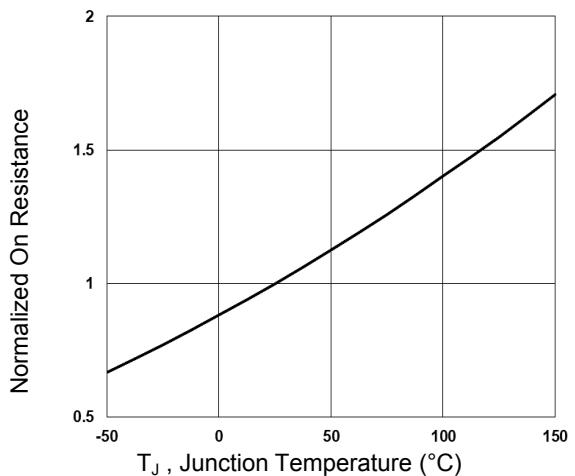


Figure 2. Normalized R_{DS(ON)} vs. T_j

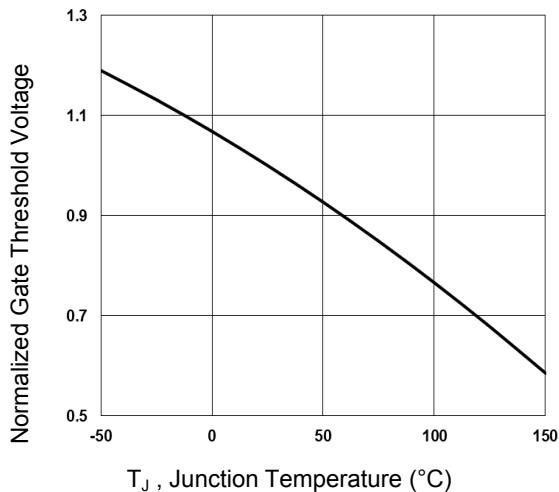


Figure 3. Normalized V_{th} vs. T_j

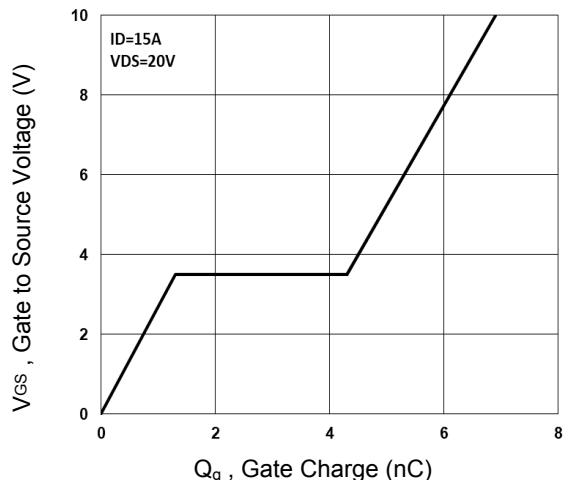


Figure 4. Gate Charge Waveform

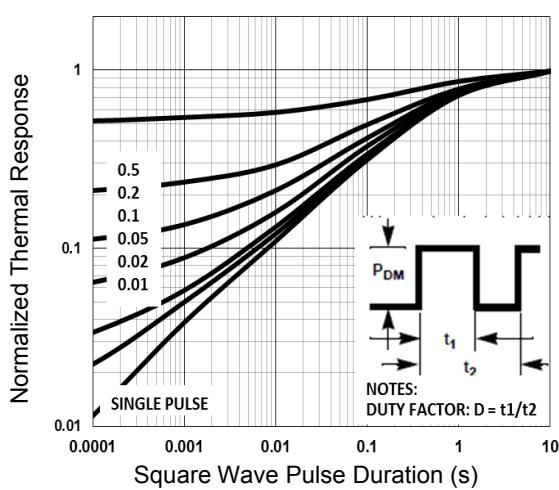


Figure 5. Normalized Transient Response

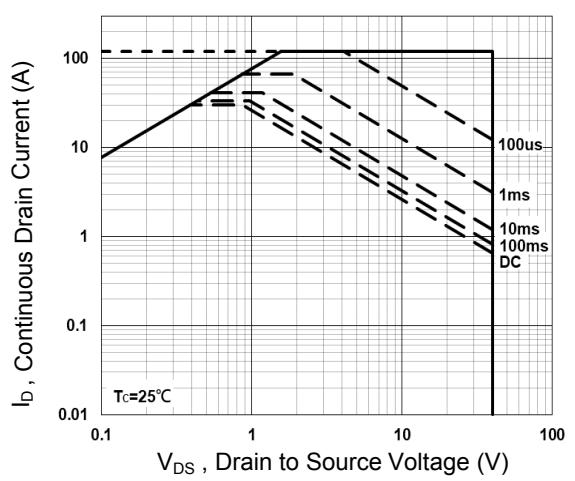


Figure 6. Maximum Safe Operation Area

Typical Electrical and Thermal Characteristic Curves

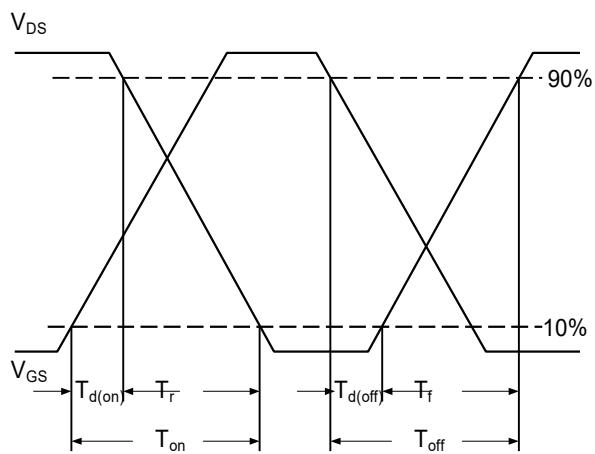


Figure 7. Switching Time Waveform

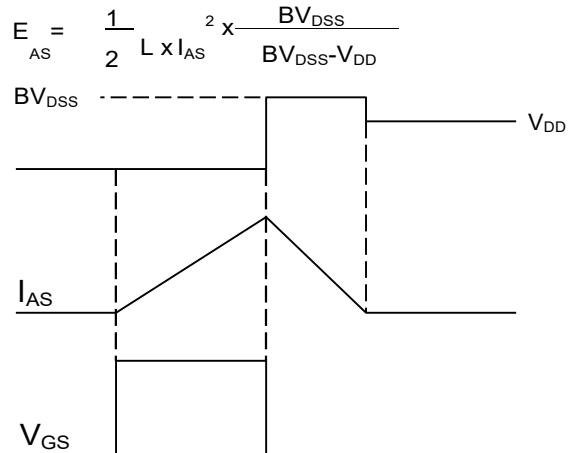
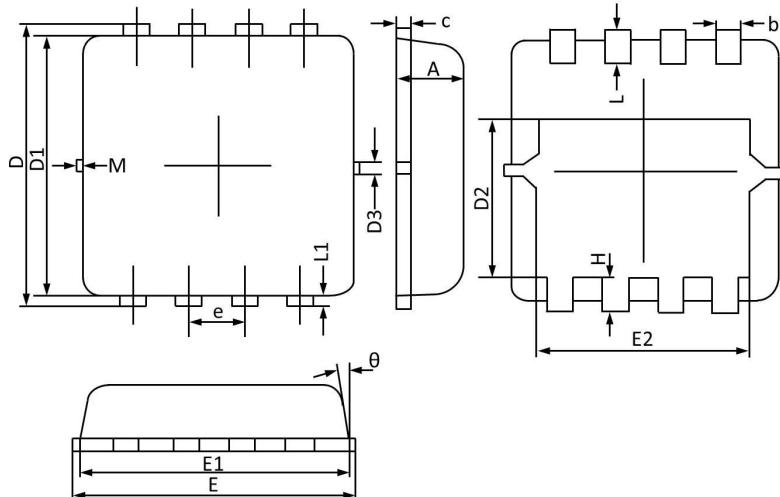


Figure 8. EAS Waveform

Package Outline Dimensions PPAK3X3



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	0.900	0.700	0.035	0.028
b	0.350	0.240	0.014	0.009
c	0.250	0.100	0.010	0.004
D	3.450	3.050	0.136	0.120
D1	3.200	2.900	0.126	0.114
D2	1.850	1.350	0.073	0.053
E	3.400	3.000	0.134	0.118
E1	3.250	2.900	0.128	0.114
E2	2.600	2.350	0.102	0.093
e	0.65BSC		0.026BSC	
H	0.500	0.300	0.020	0.012
L	0.500	0.300	0.020	0.012
L1	0.200	0.070	0.008	0.003
θ	12°	0°	12°	0°

Recommended Pad Layout

