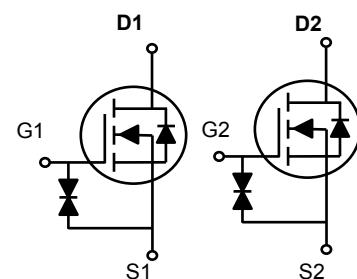


Main Product Characteristics

| | |
|---------------------|-------|
| BV _{DSS} | 30V |
| R _{DS(ON)} | 450mΩ |
| I _D | 600mA |



Features and Benefits

SOT-563

Schematic Diagram

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSFJ03600 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supply and a wide variety of other applications.

Absolute Maximum Ratings (T_C=25°C unless otherwise specified)

| Parameter | Symbol | Max. | Unit |
|---|------------------|-------------|-------|
| Drain-Source Voltage | V _{DS} | 30 | V |
| Gate-Source Voltage | V _{GS} | ±12 | V |
| Drain Current-Continuous (T _A =25°C) | I _D | 600 | mA |
| Drain Current-Continuous (T _A =70°C) | | 480 | |
| Drain Current-Pulsed ¹ | I _{DM} | 2.4 | A |
| Power Dissipation (T _A =25°C) | P _D | 313 | mW |
| Power Dissipation-Derate above 25°C | | 2.5 | mW/°C |
| Thermal Resistance, Junction-to-Ambient | R _{θJA} | 400 | °C/W |
| Operating Junction Temperature Range | T _J | -55 To +150 | °C |
| Storage Temperature Range | T _{STG} | -55 To +150 | °C |

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

| Parameter | Symbol | Conditions | Min. | Typ. | Max. | Unit |
|---|--------------------------|--|------|------|----------|------------------|
| On / Off Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV_{DSS} | $V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$ | 30 | - | - | V |
| Drain-Source Leakage Current | I_{DSS} | $V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$ | - | - | 1 | μA |
| | | $V_{\text{DS}}=24\text{V}, V_{\text{GS}}=0\text{V}, T_J=125^\circ\text{C}$ | - | - | 10 | μA |
| Gate-Source Leakage Current | I_{GSS} | $V_{\text{GS}}=\pm 12\text{V}, V_{\text{DS}}=0\text{V}$ | - | - | ± 20 | μA |
| Static Drain-Source On-Resistance | $R_{\text{DS(ON)}}$ | $V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=0.3\text{A}$ | - | 370 | 450 | $\text{m}\Omega$ |
| | | $V_{\text{GS}}=2.5\text{V}, I_{\text{D}}=0.2\text{A}$ | - | 510 | 650 | |
| Gate Threshold Voltage | $V_{\text{GS(th)}}$ | $V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250\mu\text{A}$ | 0.5 | 0.7 | 1.2 | V |
| Forward Transconductance | g_{fs} | $V_{\text{DS}}=4\text{V}, I_{\text{D}}=0.3\text{A}$ | - | 0.8 | - | S |
| Dynamic and Switching Characteristics | | | | | | |
| Total Gate Charge ^{2,3} | Q_g | $V_{\text{DS}}=15\text{V}, I_{\text{D}}=0.3\text{A}$ $V_{\text{GS}}=4.5\text{V}$ | - | 2.6 | 5.2 | nC |
| Gate-Source Charge ^{2,3} | Q_{gs} | | - | 0.9 | 1.8 | |
| Gate-Drain Charge ^{2,3} | Q_{gd} | | - | 0.6 | 1.2 | |
| Turn-On Delay Time ^{2,3} | $t_{\text{d(on)}}$ | $V_{\text{DD}}=15\text{V}, R_{\text{G}}=10\Omega$ $V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=0.3\text{A}$ | - | 5.5 | 11 | nS |
| Rise Time ^{2,3} | t_r | | - | 4 | 8 | |
| Turn-Off Delay Time ^{2,3} | $t_{\text{d(off)}}$ | | - | 14.5 | 29 | |
| Fall Time ^{2,3} | t_f | | - | 6.5 | 13 | |
| Input Capacitance | C_{iss} | $V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, F=1\text{MHz}$ | - | 43 | 65 | pF |
| Output Capacitance | C_{oss} | | - | 16 | 25 | |
| Reverse Transfer Capacitance | C_{rss} | | - | 8.5 | 15 | |
| Drain-Source Diode Characteristics and Maximum Ratings | | | | | | |
| Continuous Source Current | I_s | $V_G=V_D=0\text{V},$ Force Current | - | - | 0.6 | A |
| Pulsed Source Current | I_{SM} | | - | - | 1.2 | A |
| Diode Forward Voltage | V_{SD} | $V_{\text{GS}}=0\text{V}, I_s=0.3\text{A}, T_J=25^\circ\text{C}$ | - | - | 1 | V |

Note:

1. Repetitive rating: Pulsed width limited by maximum junction temperature.
2. Pulse test: pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.

Typical Electrical and Thermal Characteristic Curves

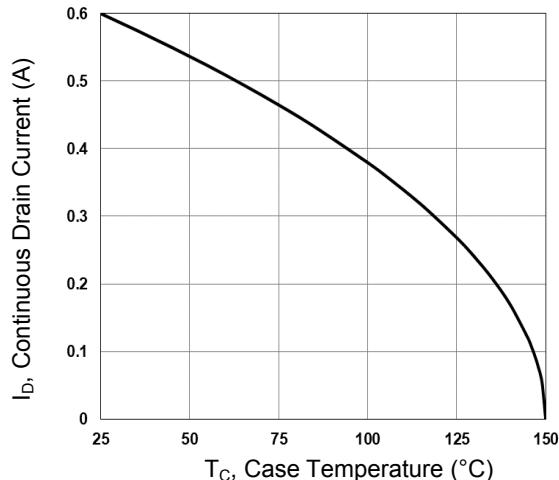


Figure 1. Continuous Drain Current vs. T_c

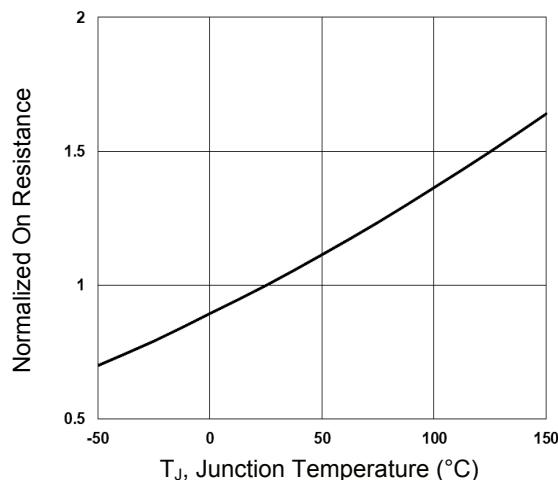


Figure 2. Normalized R_{DS(on)} vs. T_j

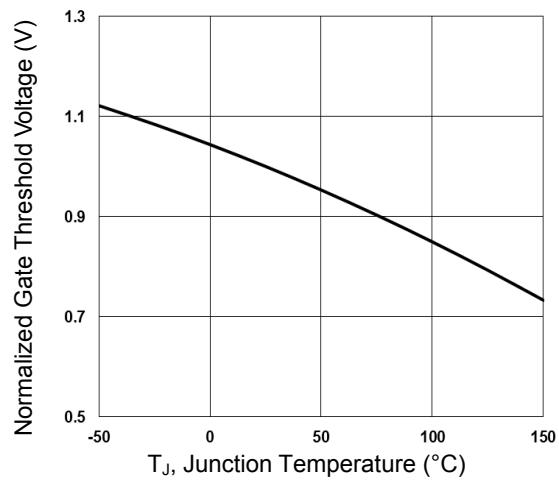


Figure 3. Normalized V_{th} vs. T_j

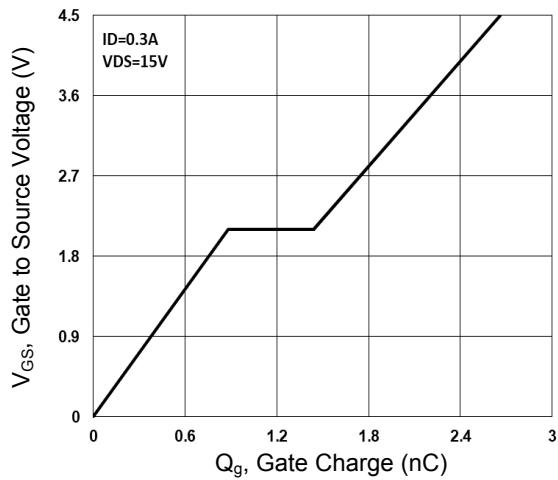


Figure 4. Gate Charge Characteristics

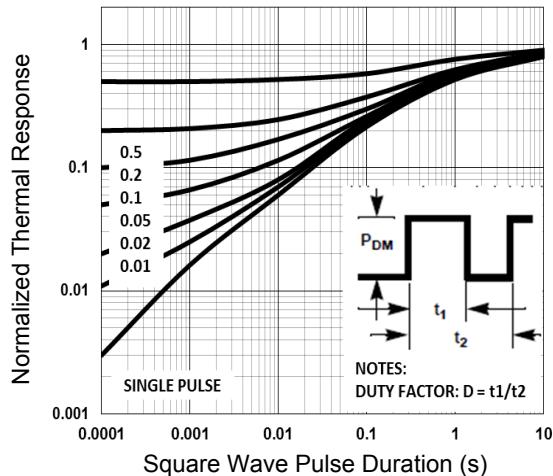


Figure 5. Normalized Transient Impedance

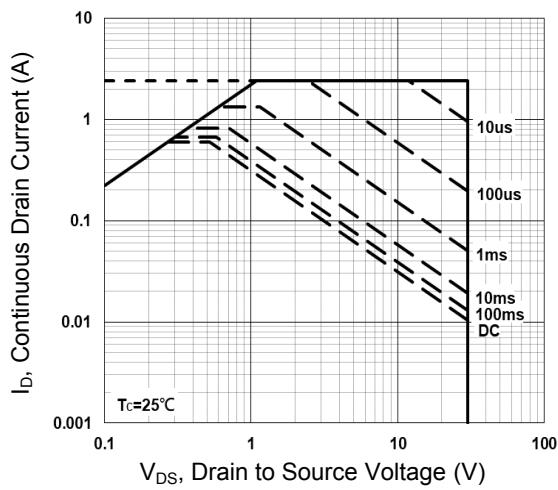
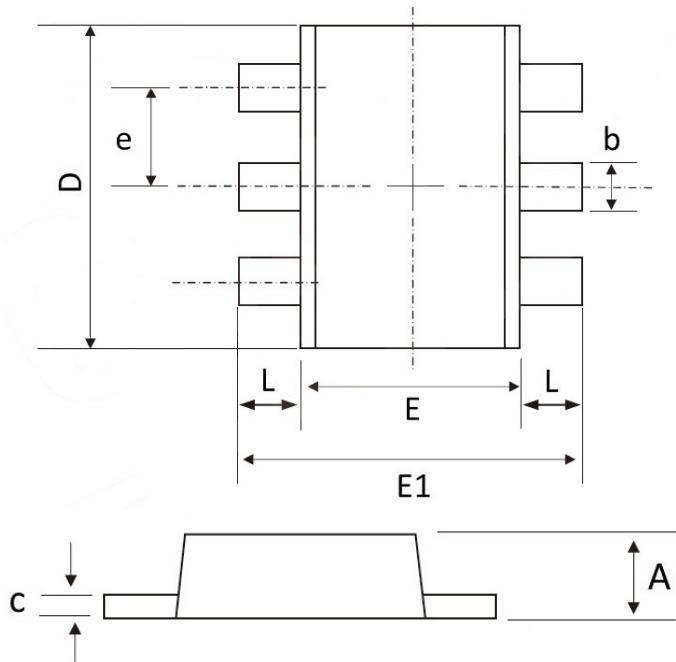


Figure 6. Maximum Safe Operation Area

Package Outline Dimensions (SOT-563)



| Symbol | Dimensions in Millimeters | | Dimensions in Inches | |
|--------|---------------------------|-------|----------------------|-------|
| | Max | Min | Max | Min |
| A | 0.600 | 0.500 | 0.024 | 0.020 |
| b | 0.300 | 0.150 | 0.012 | 0.006 |
| c | 0.180 | 0.100 | 0.007 | 0.004 |
| D | 1.700 | 1.500 | 0.067 | 0.059 |
| E | 1.250 | 1.100 | 0.049 | 0.043 |
| E1 | 1.700 | 1.550 | 0.067 | 0.061 |
| e | 0.500 BSC | | 0.020 BSC | |
| L | 0.300 | 0.100 | 0.012 | 0.004 |