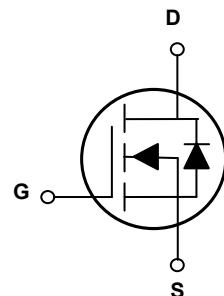


### Main Product Characteristics

$V_{(BR)DSS}$	900V
$R_{DS(ON)}$	1.2Ω (Max.)
$I_D$	5A



TO-262



Schematic Diagram

### Features and Benefits

- Advanced MOSFET process technology
- Low drain-to-source voltage drop
- Fast switching and reverse body recovery



### Description

The GSFDT90R120 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

### Absolute Maximum Ratings ( $T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	$V_{DS}$	900	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Drain Current-Continuous <sup>1</sup> ( $T_C=25^\circ\text{C}$ )	$I_D$	5	A
Drain Current-Continuous <sup>1</sup> ( $T_C=100^\circ\text{C}$ )		3.2	
Drain Current-Pulsed <sup>2</sup> ( $T_C=25^\circ\text{C}$ )	$I_{D,pulse}$	15	A
Continuous Diode Forward Current <sup>1</sup> ( $T_C=25^\circ\text{C}$ )	$I_S$	5	A
Diode Pulsed Current <sup>2</sup> ( $T_C=25^\circ\text{C}$ )	$I_{S,pulse}$	15	A
Power Dissipation <sup>3</sup> ( $T_C=25^\circ\text{C}$ )	$P_D$	83	W
Single Pulsed Avalanche Energy <sup>5</sup>	$E_{AS}$	193	mJ
MOSFET dv/dt Ruggedness, $V_{DS}=0-480\text{V}$	dv/dt	50	V/ns
Reverse Diode dv/dt, $V_{DS}=0-480\text{V}$ , $I_{SD} \leq I_D$	dv/dt	15	V/ns
Thermal Resistance, Junction-to-Ambient <sup>4</sup>	$R_{\text{JA}}$	62	°C/W
Thermal Resistance, Junction-to-Case	$R_{\text{JC}}$	1.5	°C/W
Junction Temperature Range	$T_J$	-55 To +150	°C
Storage Temperature Range	$T_{STG}$	-55 To +150	°C

**Electrical Characteristics** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>On / Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	900	-	-	V
		$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$ $T_J=150^\circ\text{C}$	960	1070	-	V
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}}=900\text{V}, V_{\text{GS}}=0\text{V}$	-	-	10	$\mu\text{A}$
Gate-Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 30\text{V}$	-	-	$\pm 100$	nA
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=2\text{A}$	-	1.0	1.2	$\Omega$
		$V_{\text{GS}}=10\text{V}, I_{\text{D}}=2\text{A},$ $T_J=150^\circ\text{C}$	-	2.88	-	
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250\mu\text{A}$	2.0	-	4.0	V
<b>Dynamic and Switching Characteristics</b>						
Total Gate Charge	$Q_g$	$V_{\text{DS}}=400\text{V}, I_{\text{D}}=5\text{A},$ $V_{\text{GS}}=10\text{V}$	-	12.5	-	nC
Gate-Source Charge	$Q_{gs}$		-	3.8	-	
Gate-to-Drain Charge	$Q_{gd}$		-	4.3	-	
Gate Plateau Voltage	$V_{\text{plateau}}$		-	5.8	-	
Turn-On Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DS}}=400\text{V}, R_{\text{G}}=33\Omega,$ $V_{\text{GS}}=10\text{V}, I_{\text{D}}=5\text{A}$	-	33.2	-	nS
Rise Time	$t_r$		-	26.5	-	
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	44	-	
Fall Time	$t_f$		-	17.6	-	
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=50\text{V}, V_{\text{GS}}=0\text{V},$ $f=100\text{kHz}$	-	874	-	pF
Output Capacitance	$C_{\text{oss}}$		-	37.5	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	1.7	-	
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
Peak Reverse Recovery Current	$I_{\text{rrm}}$	$V_R=400\text{V}, I_S=5\text{A},$ $dI/dt=100\text{A}/\mu\text{s}$	-	19.5	-	A
Reverse Recovery Time	$T_{\text{rr}}$		-	265.9	-	ns
Reverse Recovery Charge	$Q_{\text{rr}}$		-	2.9	-	uC
Diode Forward Voltage	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=5\text{A}$	-	-	1.3	V

Note:

- Calculated continuous current based on maximum allowable junction temperature.
- Repetitive rating; pulse width limited by max. junction temperature.
- $P_d$  is based on max. junction temperature, using junction-case thermal resistance.
- The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with  $T_A=25^\circ\text{C}$ .
- $V_{DD}=100\text{V}, V_{GS}=10\text{V}, L=79.9\text{mH}$ , starting  $T_J=25^\circ\text{C}$ .

## Typical Electrical and Thermal Characteristic Curves

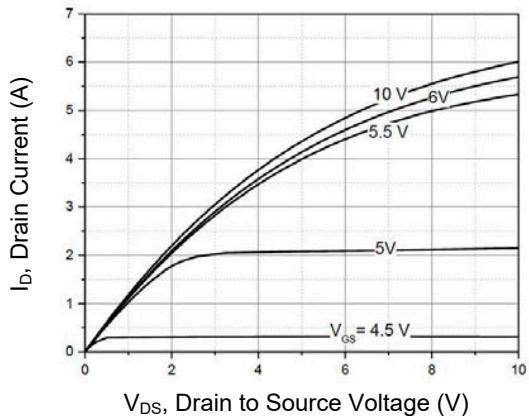


Figure 1. Output Characteristics

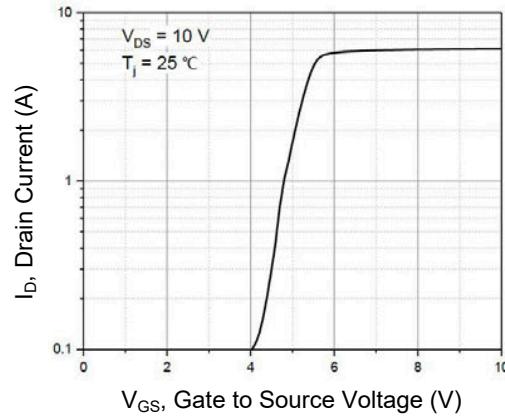


Figure 2. Transfer Characteristics

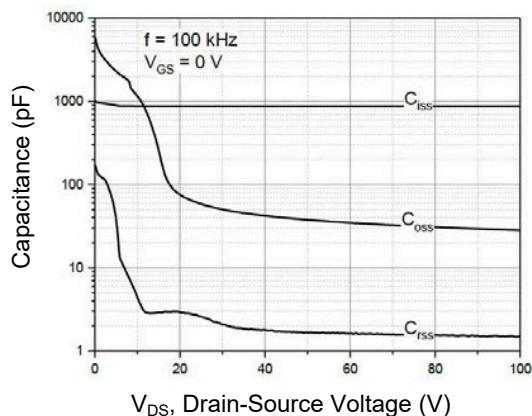


Figure 3. Capacitance Characteristics

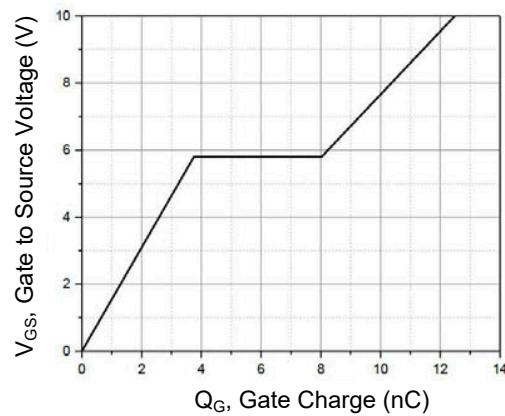


Figure 4. Gate Charge

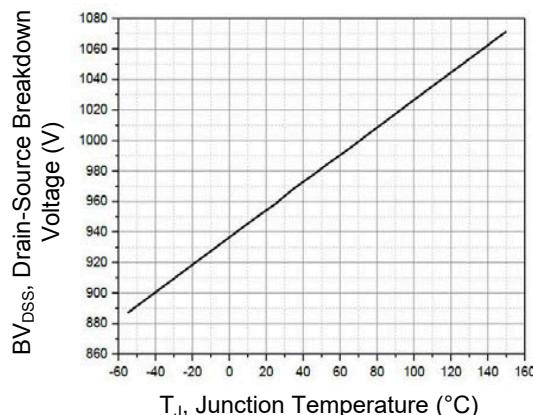


Figure 5. Drain-Source Breakdown Voltage

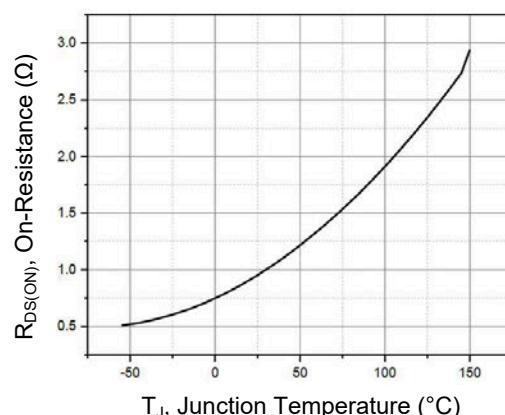


Figure 6. Drain-Source On-State Resistance

## Typical Electrical and Thermal Characteristic Curves

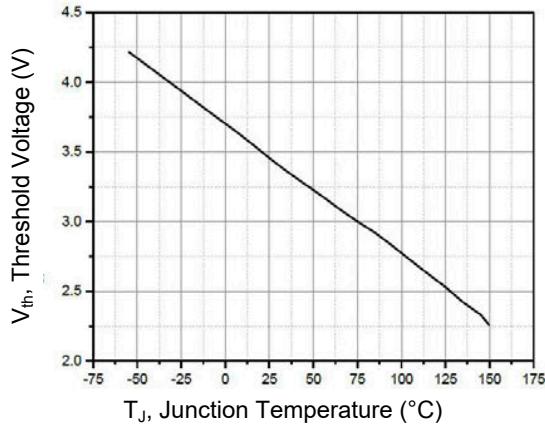


Figure 7. Threshold Voltage

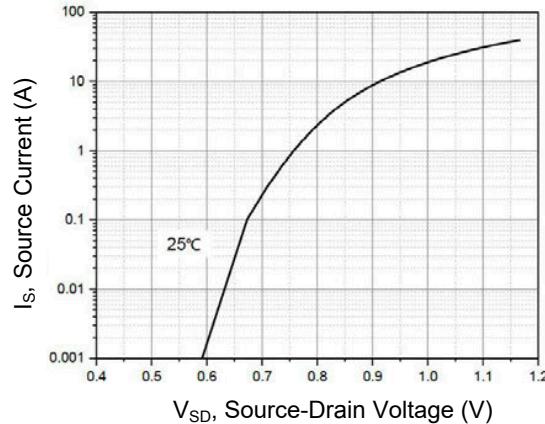


Figure 8. Forward Characteristics of Body Diode

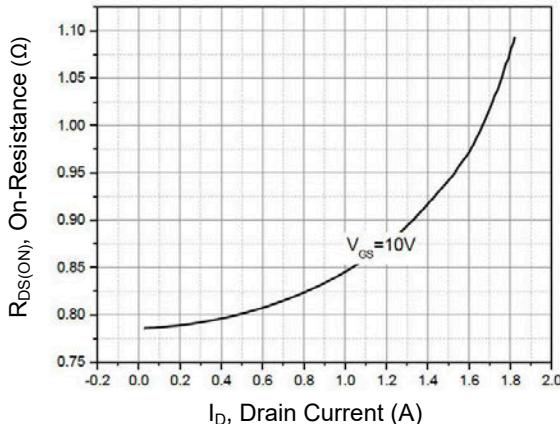


Figure 9. Drain-Source On-State Resistance

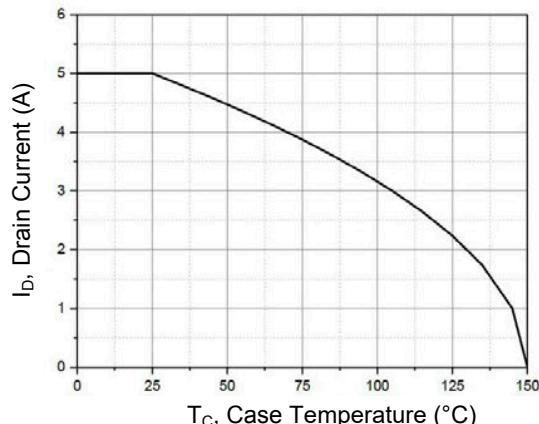


Figure 10. Drain Current

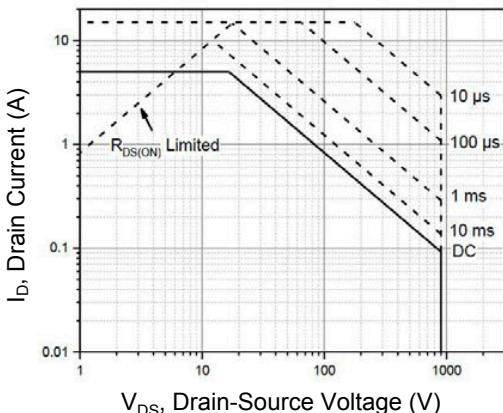
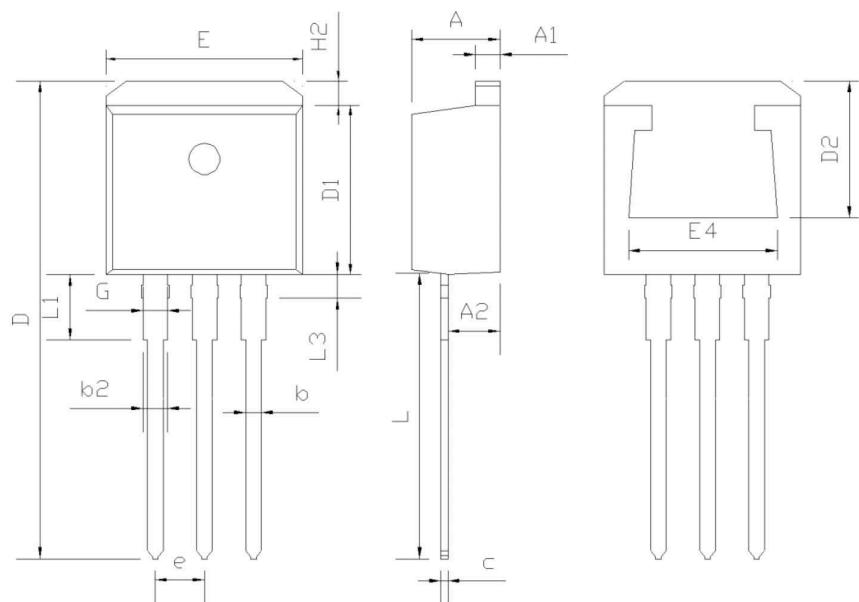


Figure 11. Safe Operation Area, T<sub>c</sub>=25°C

### Package Outline Dimensions (TO-262)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	4.370	4.770	0.172	0.188
A1	1.220	1.420	0.048	0.056
A2	2.490	2.890	0.098	0.114
b	0.710	0.960	0.028	0.038
b2	1.170	1.420	0.046	0.056
c	0.280	0.530	0.011	0.021
D	23.200	24.020	0.913	0.946
D1	8.500	8.900	0.335	0.350
D2	6.000	-	0.236	-
E	9.860	10.360	0.388	0.408
E4	7.060	-	0.278	-
e	2.540 BSC		0.100 BSC	
H2	-	1.500	-	0.059
L	13.330	14.130	0.525	0.556
L1	3.500	4.000	0.138	0.157
L3	1.280	1.580	0.050	0.062
G	1.250	1.500	0.049	0.059